

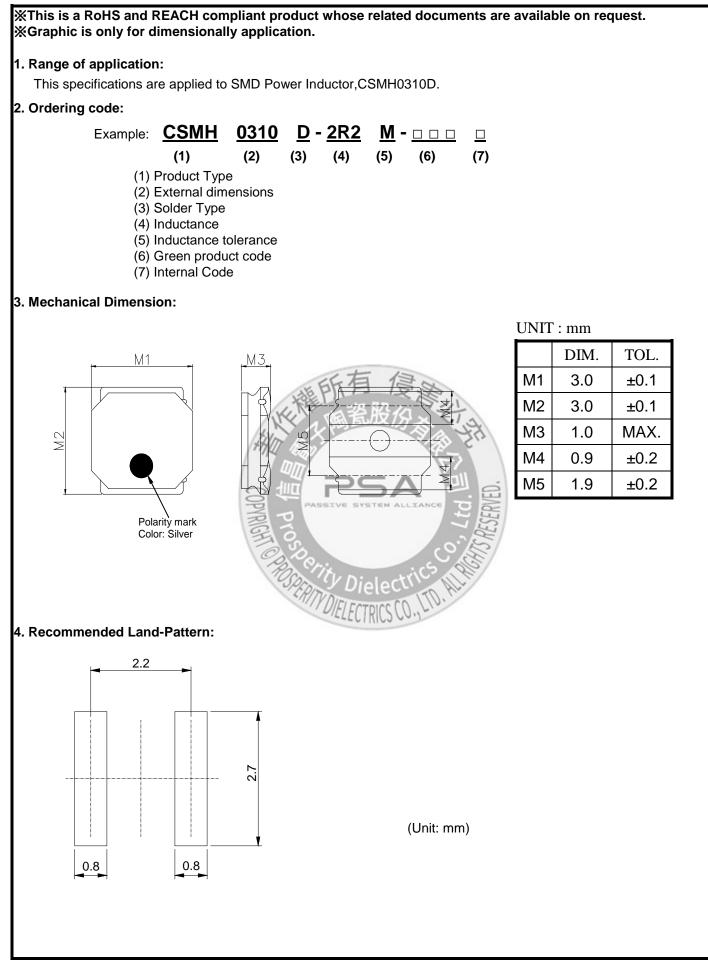
CUSTOMER	
CUST. PART NO.	
CUST. DOC. REV.	··
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
SAMPLE LOT NO.	
PART NO.	CSMH0310D-XXXX-LRH
DOC. REV.	
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Customer Signature:	Date:
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☐This part currently development section.	Production line can produce this series of products.
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CUSTOMER		CUSTOMER P/N	REV.		LOT NO.		
PART NAME POWER CI (ROHS+I		PART NO. CSMH0310D-XXXX-LRH	REV.	DATI	E OF ISSUE	Q'TY 0	PCS
	ENG	INEERING CHANG	GE NO	TIC	E - RECO	ORD	
REVISION NO.		REVISION DESCRIPTIO	DN		AUTHOR	DATE	REMARK
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5. Electrical Characteristics:

Part Number	Nominal Inductance I	Inductance	DC Resistance	Rated Current (mA)		Self-resonant Frequency
	(uH) @100KHz	+20%		Saturation Current Idc1	Temperature Rise Current Idc2	(MHz) Min.
CSMH0310D-1R2N-LRH	1.2	± 30%	0.065	1700	1480	120
CSMH0310D-1R5N-LRH	1.5	±30%	0.075	1440	1370	99
CSMH0310D-2R2M-LRH	2.2	±20%	0.083	1300	1300	86
CSMH0310D-3R3M-LRH	3.3	±20%	0.130	1000	1030	64
CSMH0310D-4R7M-LRH	4.7	±20%	0.170	850	900	50
CSMH0310D-6R8M-LRH	6.8	±20%	0.250	700	745	44
CSMH0310D-100M-LRH	10	±20%	0.350	600	620	34
CSMH0310D-150M-LRH	15	±20%	0.550	450	480	25
CSMH0310D-220M-LRH	22	±20%	0.770	380	410	22
CSMH0310D-470M-LRH	47	±20%	2.050	250	285	17

1. Test Frequency:100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent. DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

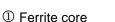
3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current ldc2: The value of current causes a 40°C temperature rise.

5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

- 6. Operating Temperature Range:-25°C to +120°C (Including self-temperature rise)
- 7. Storage Temp. Range : -40° C to $+85^{\circ}$ C.
- 8. MSL : Level 1

6.Structural Drawing :

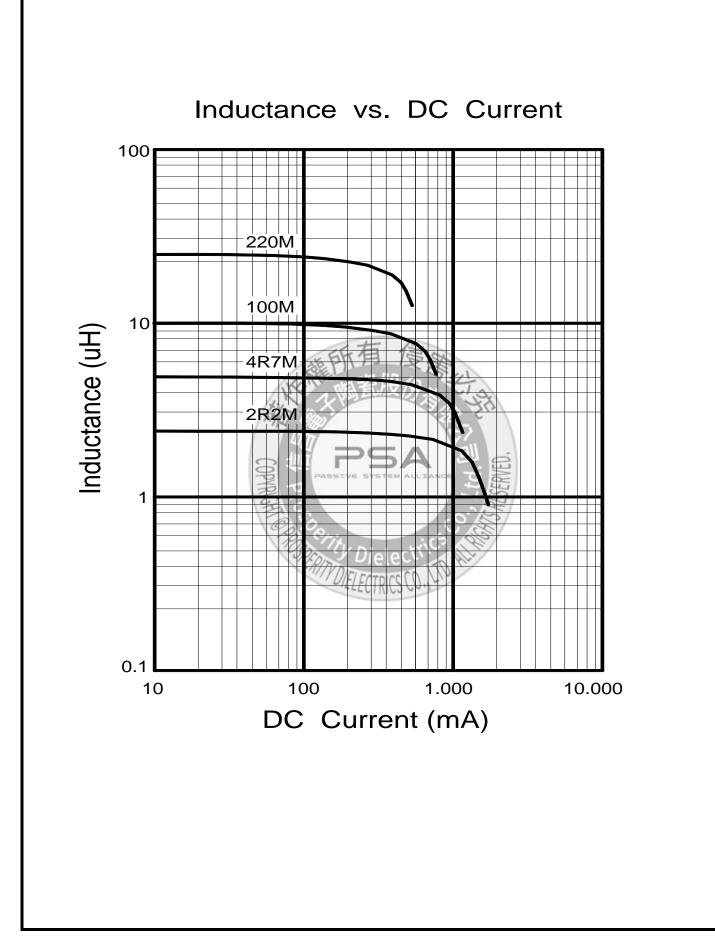


Ni-Zn	ferrite	

② Winding wirePolyurethane-copper wire③ Over-coating resinEpoxy resin, containing ferrite powder④ ElectrodeExternal electrode (substrate)AgExternal electrode (base plating)Ni-SnExternal electrode (top surface solder coating)Sn-Ag-Cu

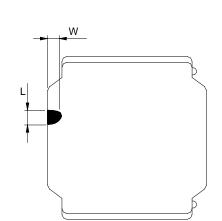
(Magnetic Shielded Type)

7. Electrical Curve :



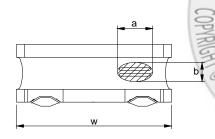
8.Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension



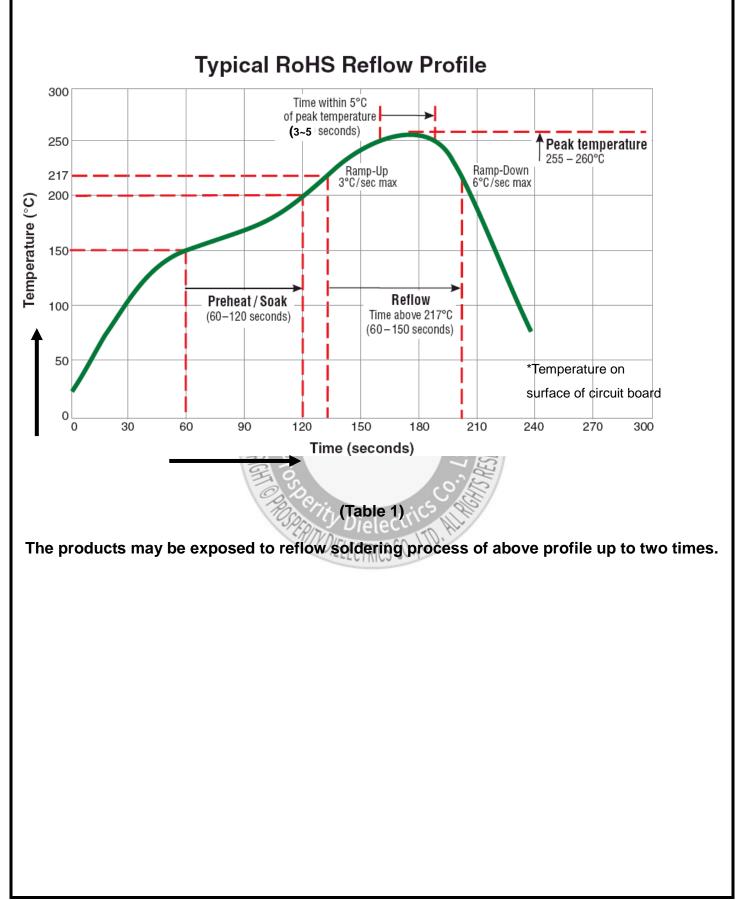
L	W	
0.6mmMax.	0.6mmMax.	

9. Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



 Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2
 Length direction (dimension b): Dimension b is not specified.
 When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

10. Reflow Profile Chart (Reference):



	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
ISTICS			$R5 \xrightarrow{20} \\ Force \\ Rod \\ Rod \\ Rod \\ Rod \\ Test \\ Sample \\ 45\pm2 \\ 45\pm2 \\ 45\pm2 \\ 45\pm2 \\ 45\pm2 \\ 1.5 \\ $
TERI			Land dimensions Test board size :100×40×10
ACI			Test board material I: glass epoxy-resin
HAR.	Adhesion of	Shall not come off PC	Solder cream thickness:0.1 Unit: mm
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.
	Dody of root of h	COPYRIGHT PASSE	Duration:5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s

Test Item	Standard	Test method		
Resistance to Vibration	△L/L:within±10% No abnormality	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then It shall be submitted to below test conditions		
	observed	Frequency range 10Hz~55Hz		
	In appearance	Total Amplitude 1.5mm(May not exceed acceleration 196 m/S ²)		
		Sweeping Method10Hz to 55Hz to 10 Hz for 1 min.TimeFor 2 hours on each X,Y, and Z axis.		
Resistance to	\triangle L/L:within±10%	The test sample shall be exposed to reflow oven at		
Soldering heat	No abnormality	230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times.		
(Reflow)	observed	Test board thickness:1.0 mm		
	In appearance	Test board material :glass epoxy-resin		
Solder ability	At least 90% of surface	The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table.		
	of terminal electrode is	Flux: Methanol solution containing rosin 25%		
	covered by new solder.	Solder Temperature 245±deg C		
	48 F	Time5±1.0 S.Immersing Speed25 mm/s		
Temperature Characteristics Thermal shock	 △L/L:within±20% No abnormality observed In appearance △L/L:within±10% No abnormality observed In appearance 	Measurement of inductance shall be taken at temperature Range within -25 deg C to +85 deg C.With reference to inductance value at +20 deg C, change Rate shall be calculated.The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence.The temperature cycle shall be repeated 100 cycles.Conditions of steps for 1 cycleStep Temperature Time(min) 1 -40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum		
Low Temperature life Test	 △L/L:within±10% No abnormality observed In appearance 	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.After that, the test samples shall be placed at test Conditions as shown in below table.Temperature-40±2 deg CTime500 +24/-0 h		

	Test Item	Standard	Test method			
	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	soldering co The test sar specified te	nditions shown in T	ered to the test board by the reflow Table 1. ed in thermostatic oven set at lied the rated current continuously	
			Г	Temperature	85±2 deg C	
				Applied current	Rated current (Refer to Page 2)	
				Time	500+24/-0 h	
TESTS	Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	soldering co The test sar	nditions shown in T nples shall be place mperature and hum	ed in thermostatic oven set at hidity as shown in below table.	
ENT				Temperature	60±2 deg C 90~95%RH	
MN		AB F	乐月 復	Humidity		
ENVIRONMENT TESTS		AND THE REAL	周瓷股份	Time	500+24/-0 h	
	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	soldering co The test sar specified te	onditions shown in T mples shall be place mperature and hum y as shown in below	ed in thermostatic oven set at nidity and applied the rated current w table.	
		C P Cri		Temperature	60±2 deg C 90~95%RH	
		COPERIN L	V Dielect VELECTRICS (Humidity Applied current Time	Rated current (Refer to Page 2)) 500+24/-0 h	

