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CUSTOMER CUST PART NO	
CUST DOC REV	
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
SAMPLE LOT NO.	
PART NO.	CSMH0312D-XXXX-LRH
DOC. REV.	
DATE	
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Once you approve this part, please sign a	nu return this page to the following market location.
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Customer Signature: 😂 😃 🔽	Date:
	SEER ALLIANCE JUST
I his part currently development section.	Production line can produce this series of products.
10sparity	Dielocti
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CUSTOMER	CUSTOMER P/N	REV.	SPL. LOT NO.		
PART NAME POWER CHOKE (ROHS+H.F.)	PART NO. CSMH0312D-XXXX-LRH	REV.	DATE OF ISSUE	Q'TY 0	PCS
EN	GINEERING CHAN	IGE NO	OTICE - REC	ORD	
REVISION NO.	<b>REVISION DESCRIPTION</b>	ON	AUTHOR	DATE	REMARK
	大日本語目示を	夏 侵害 股份有限 <b>5A</b>	E 71 W		
	Prosperity Di Bassive su	electrics	THE O. I to		



#### 5. Electrical Characteristics:

Part Number	Nominal Inductance Ir	Inductance Tolerance	Inductance Tolerance D.C. Resistance (Ω) ±20%	Rated Current (mA)		Self-resonant Frequency
	(uH) @100KH≂			Saturation	Temperature	(MHz)
	WINNER			Idc1	Idc2	WIIII.
CSMH0312D-1R0N-LRH	1.0	±30%	0.048	2200	1710	111
CSMH0312D-1R5N-LRH	1.5	±30%	0.055	1700	1600	95
CSMH0312D-2R2M-LRH	2.2	±20%	0.075	1500	1370	78
CSMH0312D-3R3M-LRH	3.3	±20%	0.100	1200	1210	61
CSMH0312D-4R7M-LRH	4.7	±20%	0.130	1000	1060	50
CSMH0312D-6R8M-LRH	6.8	±20%	0.190	850	890	43
CSMH0312D-100M-LRH	10	±20%	0.270	730	720	32
CSMH0312D-150M-LRH	15	±20%	0.450	530	570	26
CSMH0312D-220M-LRH	22	<b>±20%</b>	0.630	500	500	22

1. Test Frequency:100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent. DCR: Chroma16502 or equivalent.

SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

- 4. Temperature rise current ldc2: The value of current causes a  $40^\circ\!\mathrm{C}$  temperature rise.
- 5. Rated Current: Either Idc1 or Idc2 whichever is smaller.
- 6. Operating Temperature Range:-25°C to +120°C (Including self-temperature rise)

Ni-Zn ferrite

- 7. Storage Temp. Range : -40°C to +85°C
- 8. MSL : Level 1

#### 6. Structural Drawing:

- ① Ferrite core
- <sup>2</sup> Winding wire
- ③ Over-coating resin
- ④ Electrode

Polyurethane-copper wire	
Epoxy resin, containing ferrite powder	
External electrode (substrate)	Ag
External electrode (base plating)	Ni-Sn

à

External electrode (top surface solder coating)

(Magnetic Shielded Type)

Sn-Ag-Cu



#### 8. Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension



L	W
0.6mmMax.	0.6mmMax.

Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



 Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2
Length direction (dimension b): Dimension b is not specified.
When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

(YD)



	Test Item	Standard	Test method
	Test item	Stanuaru	
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			10 Force R230 Rod
STICS			R5 $45\pm2$ $45\pm2$ $0.8$ $1.4$ $0.8$
RIG 10			Land dimensions
CTE			Test board size :100×40×10
RA			Iest board material I: glass epoxy-resin
HA	Adhesion of	Shall not come off PC	The test samples shall be soldered to the test board
L C	Terminal	board	By the reflow soldering conditions shown in Table 1.
IC⊳	Electrode	IF THE RE	瓮股份× 5.
IAN		ANT 10	
Ц Ц		The solution	
Β			Applied force:10 N to X and Y directions
		OP PASSIN	Duration:5 s.
		BP	(Refer to recommended Land Pattern Dimensions
		H S	Defined in "Precaution")
	Body strength	No damage	Applied force :20 N
		SPERIN	
		- MINDI	R0.5mm
		011	Sample
			0.6W

Test Item	Standard	Test method
Resistance to	△L/L:within±10%	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then It shall be submitted to below test conditions
Vibration	cheenred	Frequency range 10Hz~55Hz
	In appearance	Total Amplitude     1.5mm(May not exceed acceleration 196 m/S²)
		Sweeping Method 10Hz to 55Hz to 10 Hz for 1 min.
		Time For 2 hours on each X,Y, and Z axis.
Resistance to	△L/L:within±10%	The test sample shall be exposed to reflow oven at 230+5 deg C for 40 seconds with peak temperature at
Soldering heat	No abnormality	260±5 deg C for 5 seconds, 2 times.
(Reflow)	observed	Test board thickness:1.0 mm
· · ·	In appearance	Test board material :glass epoxy-resin
Solder ability	At least 90% of surface	The test samples shall be dipped in flux, and then
	of terminal electrode is	Immersed in molten solder as shown in below table.
	covered by new solder	Solder Temperature 245±deg C
		5±1.0 S.
	大陸	Immersing Speed 25 mm/s
Temperature Characteristics	△L/L:within±20% No abnormality observed In appearance	Measurement of inductance shall be taken at temperature Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, change Rate shall be calculated.
Thermal shock	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle
	- CRITY	Step Temperature Time(min)
	1	40±3 deg C 30±3
		2 Room Temp 3 maximum
		3 85±2 deg C 30±3
		4 Room Temp 3 maximum
Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test Conditions as shown in below table.
		Temperature -40±2 deg C
		Time 500 +24/-0 h

	Test Item	Standard	Test method
	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.
			Temperature 85±2 deg C
			Applied current Rated current (Refer to Page 2)
			Time 500+24/-0 h
TESTS	Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table.
L Z			Temperature 60±2 deg C
ΜE		15 E	Humidity 90~95%RH
NOX		人物性	Time 500+24/-0 h
ENVIR		HILL IS	周围版历香展世界
E	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table.
		Con Cri	Temperature 60±2 deg C
		TOSPER .	Dielec Humidity 90~95%RH
		- MIN	Applied current Rated current (Refer to Page 2))
			Time 500+24/-0 h





