

CUSTOMER		
CUST. PART NO.		_
CUST, DOC. REV.		_
DESCRIPTION	POWER CHOKE(RoHS+H.F.)	_
	POWER CHORE(ROHS+H.F.)	_
SAMPLE LOT NO.		_
PART NO.	CSMH2410D-XXXX-LRH	
DOC. REV.		
DATE)
	+ 13	
45 日	7月10年	
大學	一次队工	
Once you approve this part, please sign a	nd return this page to the fo	llowing marked location.
Alm in	10 70	
11/1/ 2010	(F)	
PASSI	VE SYSTEM ALLIANCE	
Customer Signature:	Date:	
0,00		
☐ This part currently development section.	☐Production line can pro	duce this series of products.
ERITUR	Dielect TO HO	
Sales Office-Headquarter	Yong Zhou Plant	
No. 566-1, Kao-Shi Rd., Yangmei, Taoyuan 320		ng Dark I angshuitan
Taiwan	District, Yongzhou, Hun	
TEL: +886-3-475-3355	TEL: +86-746-8610-180	un 72000, 1.10.C.
FAX: +886-3-485-4959	FAX: +86-746-8610-181	

Sales Office-Dong Guan, China

No.638,Mei Jing West Road Xiniupo Administrative Zone Dalang Town,Dong Guan City,GuangDong

Province, China.

Once you approve

TEL: +86-769-8555-0979 FAX: +86-769-8555-0972

TESTED BY	CHECKED BY	APPROVED BY

TABLE OF CONTENTS

INDEX	Page
■ Engineering Change Notice - Record	2
■ Part Number Identification	3
■ Mechanical Dimension	3
■ Recommended Land-Pattern	3
■ Electrical Specifications	4
■ Structural Drawing	4
■ Electrical Curve	5
■ Core Chipping	6
■ Reflow Chart	7
■ Mechanical Performance	8
■ Environmental Test Performance Specifications	9 ~ 10
■ Packing	11 ~ 13
■ Test Report	

USTOMER	CUSTOMER P/N	REV.	SPL. LOT NO.		
ART NAME POWER CHOK (ROHS+H.F.)	PART NO. CSMH2410D-XXXX-LRH	REV.	DATE OF ISSUE	Q'TY	0 PCS
EN	GINEERING CHAN	GE NO	OTICE - REC	ORD	
REVISION NO.	REVISION DESCRIPTIO)N	AUTHOR	DATE	REMAR
	和海斯角	上 [長]			
	OPPRICE PROSPERITY DIE	Jectrics	O. 14		
	MINDIELECT	RICS CO., LT	0.1		

P2 Rev.A

XThis is a RoHS and REACH compliant product whose related documents are available on request.

XGraphic is only for dimensionally application.

XEVENTE:

YEVENTE:

1. Range of application:

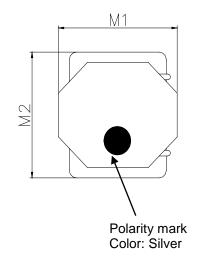
This specifications are applied to SMD Power Inductor, CSMH2410D.

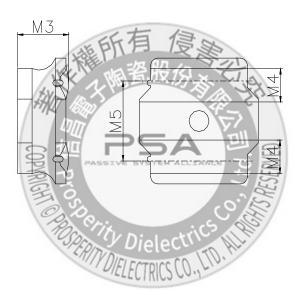
2. Ordering code:

Example: $\frac{CSMH}{(1)}$ $\frac{2410}{(2)}$ $\frac{D}{(3)}$ $\frac{2R2}{(4)}$ $\frac{M}{(5)}$ $\frac{\Box}{(6)}$ $\frac{\Box}{(7)}$

- (1) Product Type
- (2) External dimensions
- (3) Solder Type
- (4) Inductance
- (5) Inductance tolerance
- (6) Green product code
- (7) Internal Code

3. Mechanical Dimension:

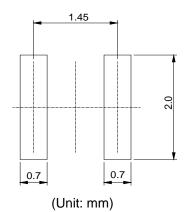




UNIT: mm

	DIM.	TOL.
M1	2.4	±0.1
M2	2.4	±0.1
М3	1.0	MAX.
M4	0.6	±0.2
M5	1.45	±0.2

4. Recommended Land-Pattern:



Caution

Excessive solder has a risk of occurrence of mounting failure like slant in consequence of reflow condition. In this case, please adjust solder quantity by design Change of stencil size and/or its thickness, etc.

Stencil thickness	%Stencil area
0.10mm	70%
0.12mm	60%
0.15mm	50%

^{*}The above values are shown as scale ratio from our recommended land pattern

5. Electrical Characteristics:

Part number	Nominal Inductance (uH) @100KHz	Inductance Tolerance	DC Resistance (Ω) ±20%	Cur	rent A) Temperature Rise Current Idc2	Self-resonant Frequency (MHz) Min.
CSMH2410D-R68N-LRH	0.68	±30%	0.06	2200	1570	120
CSMH2410D-1R0N-LRH	1.0	±30%	0.07	1800	1410	106
CSMH2410D-1R5M-LRH	1.5	±20%	0.11	1550	1160	94
CSMH2410D-2R2M-LRH	2.2	±20%	0.15	1290	970	77
CSMH2410D-3R3M-LRH	3.3	±20%	0.22	1000	770	56
CSMH2410D-4R7M-LRH	4.7	±20%	0.29	880	670	50
CSMH2410D-6R8M-LRH	6.8	±20%	0.41	750	570	43
CSMH2410D-100M-LRH	10	±20%	0.69	550	450	32
CSMH2410D-150M-LRH	15	±20%	1.02	470	370	27
CSMH2410D-220M-LRH	22	±20%	1.47	390	300	22

- 1. Test Frequency:100KHz
- 2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent.

DCR: Chroma16502 or equivalent.

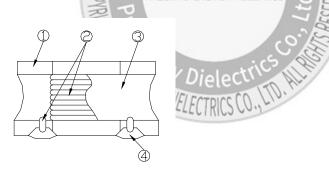
SRF: HP4291B or equivalent.

- 3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.
- 4. Temperature rise current ldc2: The value of current causes a 40℃ temperature rise.
- 5. Rated Current: Either Idc1 or Idc2 whichever is smaller.
- 6. Operating Temperature Range:-25°C to +120°C (Including self-temperature rise)

7. Storage Temp. Range: -40°C to +85°C.

8. MSL: Level 1

6. Structural Drawing:



(Magnetic Shielded Type)

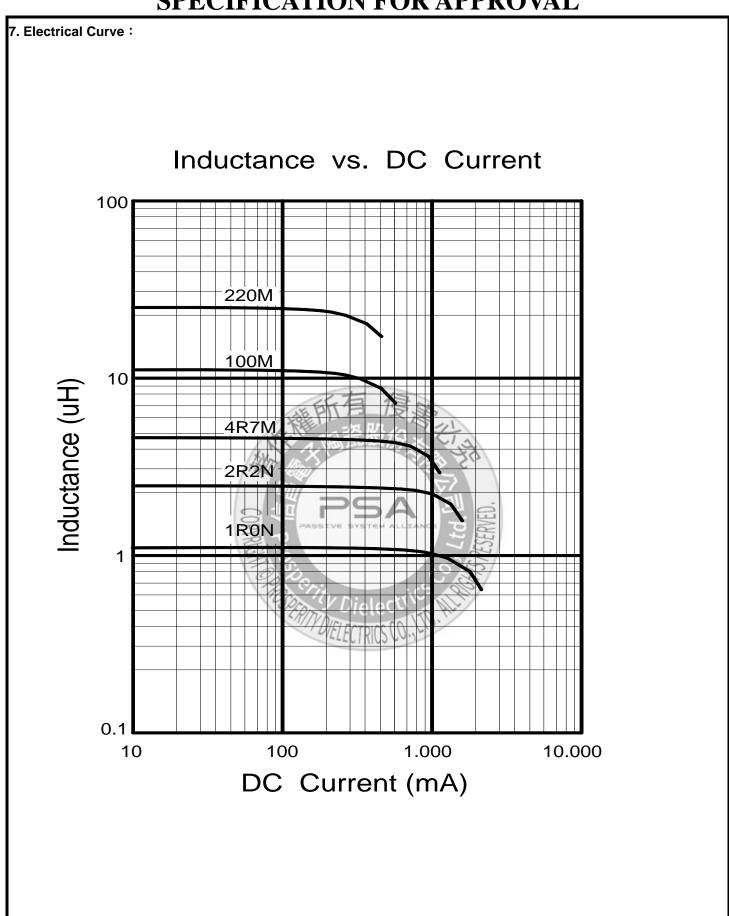
① Ferrite core Ni-Zn ferrite

② Winding wire Polyurethane-copper wire

③ Over-coating resin Epoxy resin, containing ferrite powder

④ Electrode External electrode (substrate) Cu

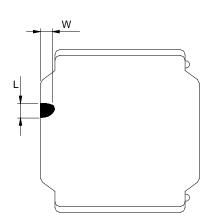
External electrode (top surface solder coating) Sn-Ag-Cu



P5 Rev.A

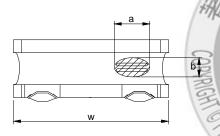
8. Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension



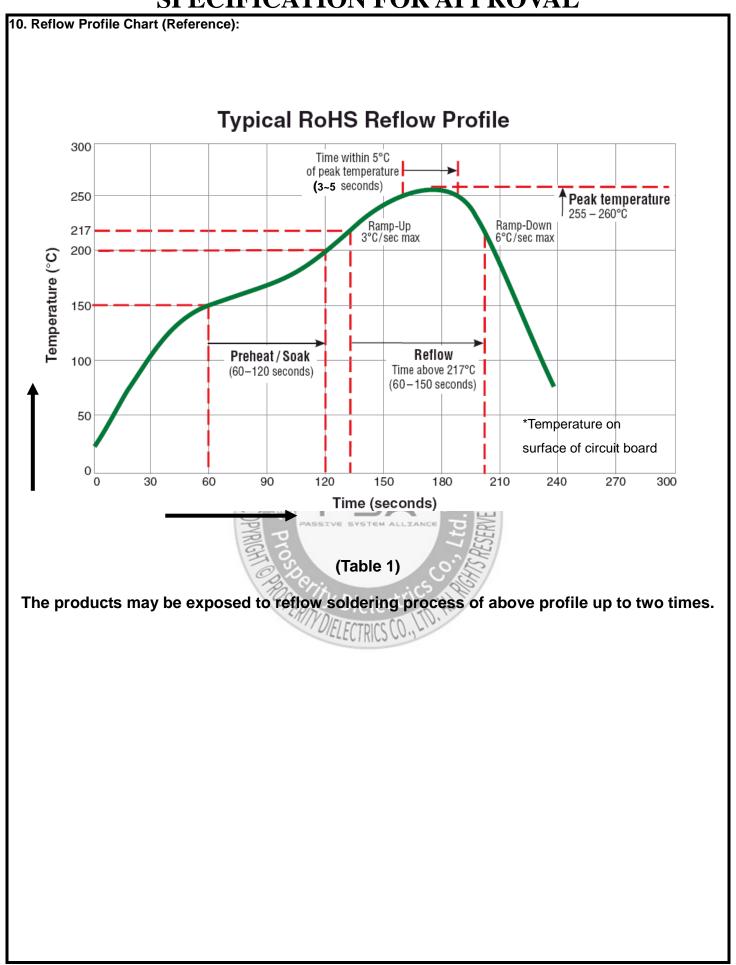
L	W
0.5mmMax.	0.5mmMax.

Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2
 Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
- 3 When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

Dielectrics (D. ID. III)

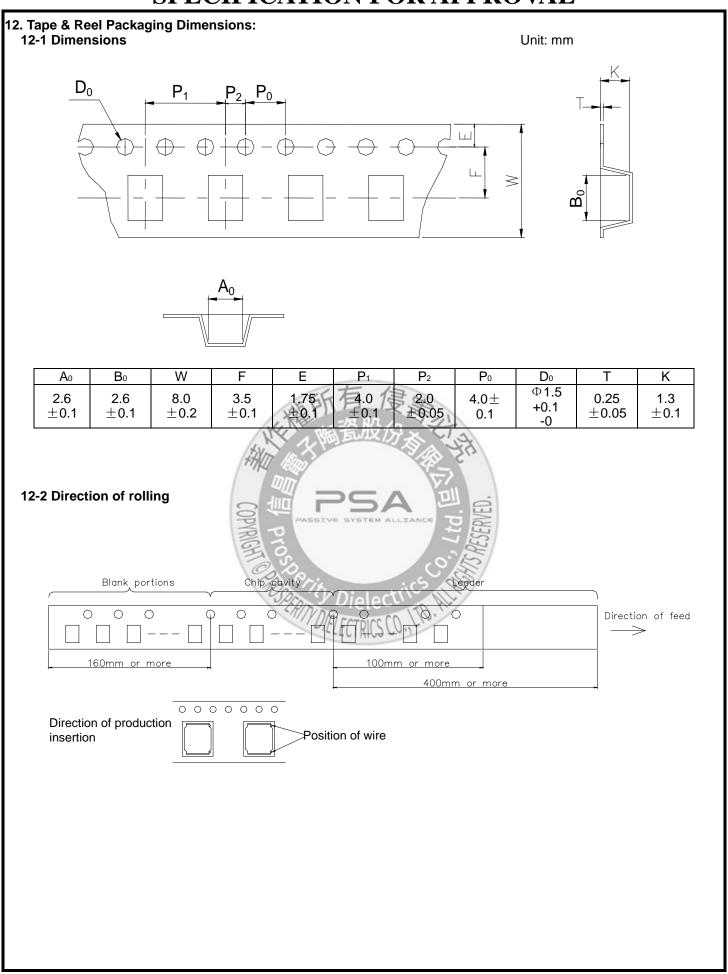


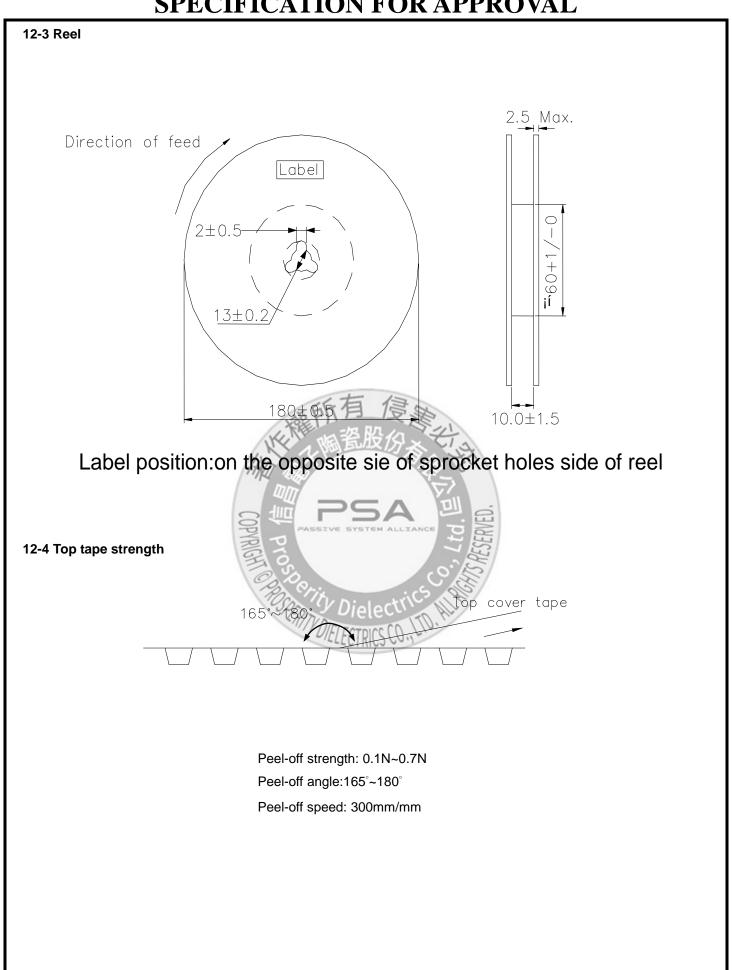
wec	nanicai Performan	ce /Environmental Test P	erformance Specifications:
	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			Force Rod 8230 5.1
STICS			R5 Board V27 Test Sample 45±2 45±2 1.5 1.5
ERIS			Land dimensions
Ç			Test board size :100x40x10 Test board material I: glass epoxy-resin
۸RA			Solder cream thickness:0.1 Unit: mm
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.
⋝			Applied force:10 N to X and Y directions Duration:5 s.
		OPYRIC	Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s

Rev.A

Test Item	Standard		Test method	
Resistance to	△L/L:within±10%	The test samples shall		
Vibration	No abnormality	The reflow soldering of It shall be submitted to		
	observed	Frequency range 10		
	In appearance		5mm(May not exceed 96 m/S²)	d acceleration
		Sweeping Method 10	Hz to 55Hz to 10 Hz	for 1 min.
		Time Fo	or 2 hours on each X,	Y, and Z axis.
Resistance to	△L/L:within±10%	The test sample shall 230±5 deg C for 40 se		
Soldering heat	No abnormality	260±5 deg C for 5 se		ilperature at
(Reflow)	observed	Test board thickness:	1.0 mm	
	In appearance	Test board material :g	lass epoxy-resin	
	The test samples shall Immersed in molten s			
	of terminal electrode is			
	covered by new solder.	Solder Temperature	245±deg C	
		Time	5±1.0 S.	
	136 F	Immersing Speed	25 mm/s	
Temperature Characteristics	△L/L:within±20% No abnormality observed In appearance	Measurement of induction Range within -25 deg With reference to induction Rate shall be calculated.	C to +85 deg C. ctance value at +20 o	
Thermal shock	△L/L:within±10% No abnormality observed In appearance	The test samples sha By the reflow solderin The test samples sha Shown in below table The temperature cycle Conditions of steps fo	g conditions shown ir Il be placed at specific in sequence. e shall be repeated 10	n Table 1. ed
	SPEDIT	Step Temperat		
	11/1/	1-0-40±3 deg	-	3
		2 Room Te		
		3 85±2 deg		
		4 Room Te	mp 3 maxin	num
Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The test samples shall The reflow soldering of After that, the test san Conditions as shown in	conditions shown in Ta nples shall be placed	able 1.
		Temperature	-40±2 deg C	
		Time	500 +24/-0 h	

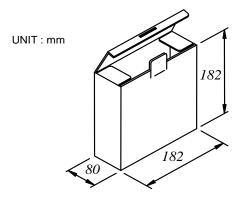
	Test Item	Standard	Test method
	Loading at high temperature life test test Loading at high		The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.
			Temperature 85±2 deg C Applied current (Refer to Page 2) Time 500+24/-0 h
ENVIRONMENT TESTS	Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Time 500+24/-0 h
ENVI	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated currer continuously as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Applied current (Refer to Page 2)) Time 500+24/-0 h





P12 Rev.A

12-5 Dimensions of packing box (for Tape & Reel package)



CONSTURCTION:

THE CASE CONTAINS 5-8mm WIDE CARRIER TAPES.



TOTAL Q'TY: 75,000 PCS