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SPECIFICATION FOR APPROVAL

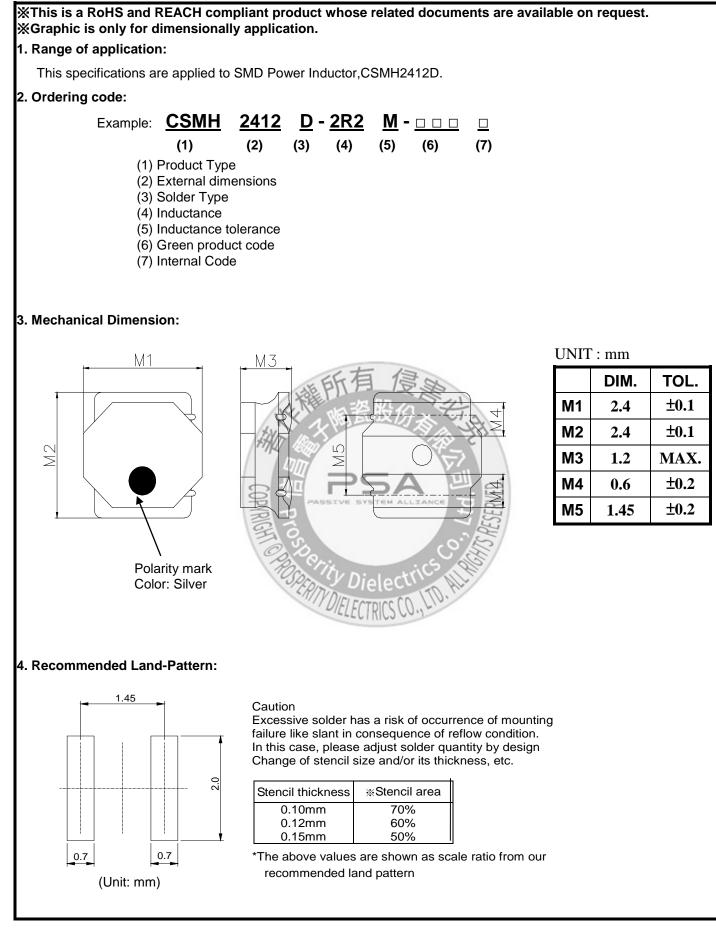
CUSTOMER	
CUST. PART NO).
CUST. DOC. RE	
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
SAMPLE LOT N	
PART NO.	CSMH2412D-XXXX-LRH
DOC. REV.	
DATE	
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Once you approve this part, please	sign and return this page to the following marked locatio
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Customer Signature:	Date:
Customer Bignature.	
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☐ This part currently development section.	Production line can produce this series of products.
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CUSTOMER	~	CUSTOMER P/N	REV.	SPL. LOT NO.		
PART NAME POWER Cl (ROHS+1		PART NO. CSMH2412D-XXXX-LRH	REV.	DATE OF ISSUE	Q'TY () PCS
	ENG	INEERING CHANG	GE NO	TICE - REC	ORD	
REVISION NO.		REVISION DESCRIPTION	N	AUTHOR	DATE	REMARK
		COPPOSITE LANGE AND	侵到 因 A EM ALLIANCE ectrics CS CO., LTD	WIS RESERVED.		



5. Electrical Characteristics:

				Ra	ted	
Part number Inc	Nominal Inductance Inductance (uH) Tolerance @100KHz	Inductance	(0)	Current (mA)		Self-resonant Frequency
		Tolerance		Saturation Current Idc1	Temperature Rise Current Idc2	Min (MHz)
CSMH2412D-R47N-LRH	0.47	±30%	0.050	2900	2100	180
CSMH2412D-1R0N-LRH	1.0	±30%	0.077	2350	1300	101
CSMH2412D-1R5N-LRH	1.5	±30%	0.100	2100	1150	89
CSMH2412D-2R2M-LRH	2.2	±20%	0.140	1700	1000	72
CSMH2412D-3R3M-LRH	3.3	±20%	0.225	1400	750	56
CSMH2412D-4R7M-LRH	4.7	±20%	0.300	1150	650	45
CSMH2412D-6R8M-LRH	6.8	±20%	0.420	950	550	34
CSMH2412D-100M-LRH	10	±20%	0.600	810	450	29

1. Test Frequency: 100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent. DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

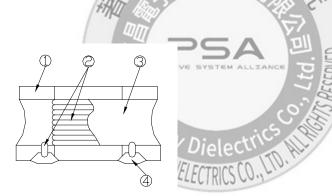
3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current ldc2: The value of current causes a $40^\circ\!C$ temperature rise.

5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

- 6. Operating Temperature Range:-25°C to +120°C (Including self-temperature rise)
- 7. Storage Temp. Range : -40° C to $+85^{\circ}$ C.
- 8. MSL : Level 1.

6. Structural Drawing :



(Magnetic Shielded Type)

① Ferrite core

(4)Electrode

Ni-Zn ferrite

- 2 Winding wire
 - y wite
- ③ Over-coating resin

..External electrode (substrate)

Epoxy resin, containing ferrite powder

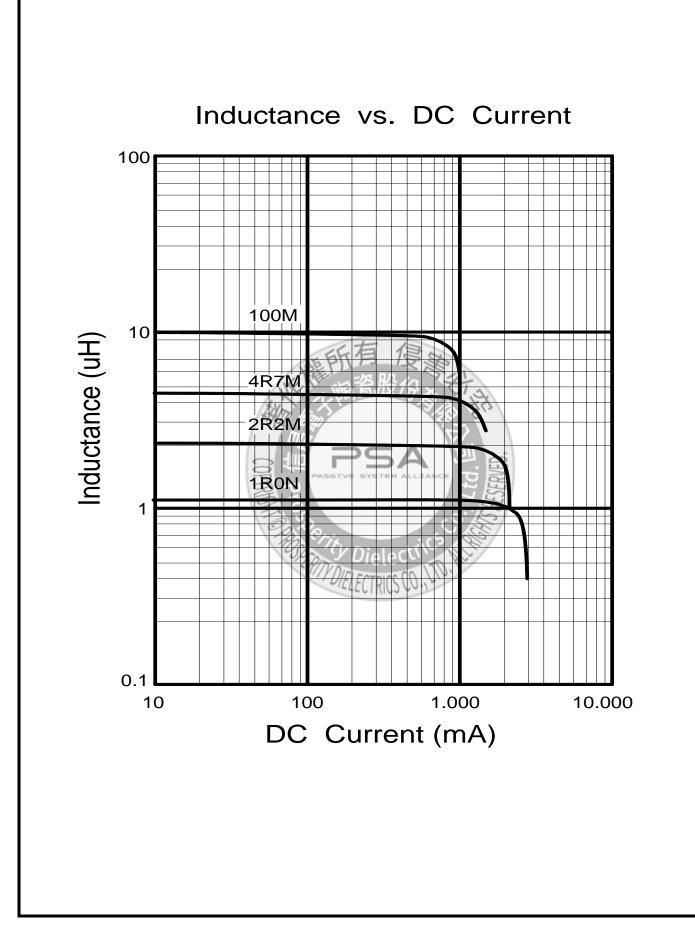
Polyurethane-copper wire

Cu

External electrode (top surface solder coating) Sr

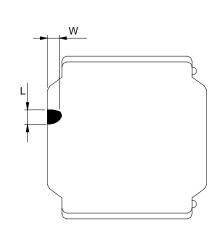
Sn-Ag-Cu

7. Electrical Curve :



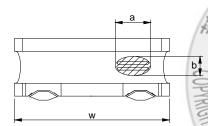
8. Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension



L	W	
0.5mmMax.	0.5mmMax.	

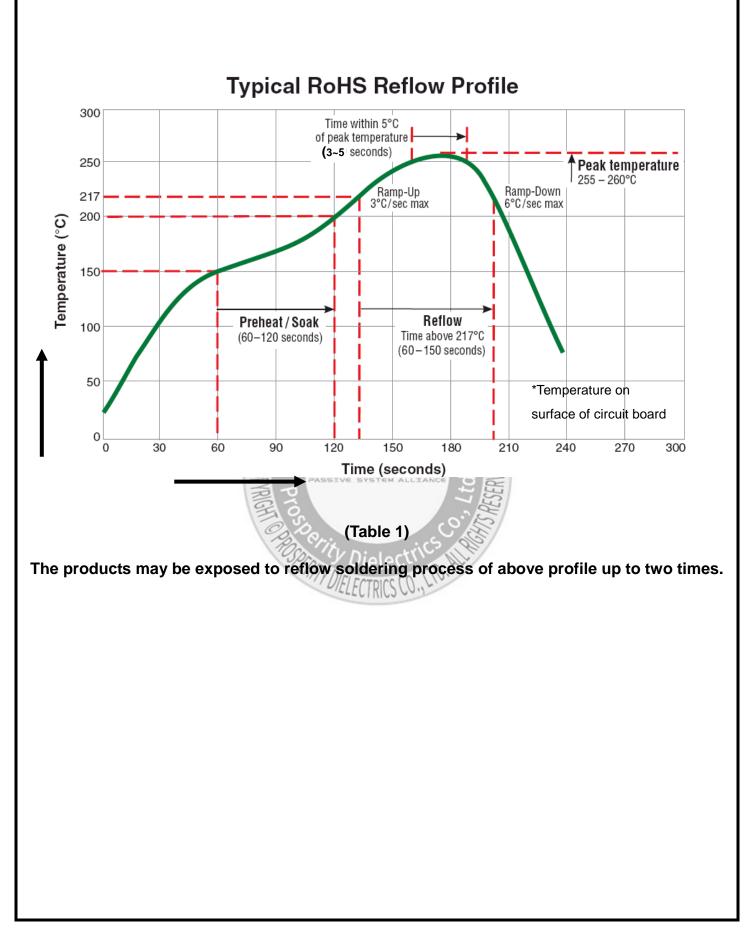
Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



 Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2
 Length direction (dimension b): Dimension b is not specified.
 When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

زن Die

10. Reflow Profile Chart (Reference):



	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	Shall not come off PC board	Land dimensions Test board size :100×40×10 Test board material I: glass epoxy-resin Solder cream thickness:0.1 Unit: mm The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1. 10 N, 5 s Applied force:10 N to X and Y directions Duration:5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s

Test Item	Standard	Test method
Resistance to	△L/L:within±10%	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then
Vibration	No abnormality	It shall be submitted to below test conditions Frequency range 10Hz~55Hz
	observed	1.5mm(May not exceed acceleration
	In appearance	196 m/S ²)
		Sweeping Method 10Hz to 55Hz to 10 Hz for 1 min.
		Time For 2 hours on each X,Y, and Z axis.
Resistance to	\triangle L/L:within±10%	The test sample shall be exposed to reflow oven at
Soldering heat	No abnormality	230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times.
(Reflow)	observed	Test board thickness:1.0 mm
	In appearance	Test board material :glass epoxy-resin
Solder ability	At least 90% of surface	The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table.
	of terminal electrode is	Flux: Methanol solution containing rosin 25%
	covered by new solder.	Solder Temperature 245±deg C
	E	Time 5±1.0 S.
	相關	Immersing Speed 25 mm/s
Temperature Characteristics	 △L/L:within±20% No abnormality observed In appearance 	Measurement of inductance shall be taken at temperature Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, change Rate shall be calculated.
Thermal shock	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles.
	Posti	Conditions of steps for 1 cycle
	SPERIN	Step Temperature Time(min)
		1 -40±3 deg C 30±3
		2 Room Temp 3 maximum
		3 85±2 deg C 30±3
		4 Room Temp 3 maximum
Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test Conditions as shown in below table.
		Temperature -40±2 deg C
		Time 500 +24/-0 h

	_		
	Test Item	Standard	Test method
	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.
			Temperature 85±2 deg C
			Applied current (Refer to Page 2)
			Time 500+24/-0 h
ESTS	Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table.
			Temperature 60±2 deg C
JEN JEN			Humidity 90~95%RH
NN		Late F	Time 500+24/-0 h
ENVIRONMENT TESTS		HIT IS	周瓷股份查测书
	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table.
		0,0	Temperature 60±2 deg C
		Posti	Humidity 90~95%RH
		SPERITY	Applied current (Refer to Page 2))
			Time 500+24/-0 h

