

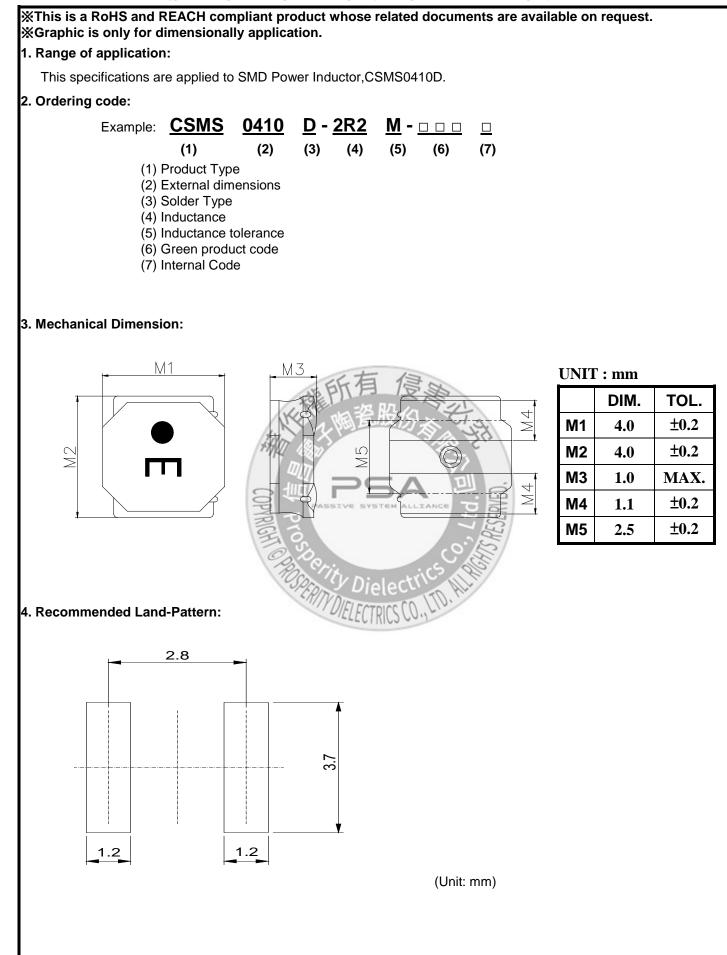
CUSTOMER	
CUST. PART NO.	
CUST. DOC. REV.	
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
SAMPLE LOT NO.	
PART NO.	CSMS0410D-XXXX-LRH
DOC. REV.	
DATE	)
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Customer Signature: 😂 🚛 🔽	Date:
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☐ This part currently development section.	<b>Production line can produce this series of products.</b>
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TESTED BY	CHECKED BY	APPROVED BY

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CUSTOMER		SPECIFICATION CUSTOMER P/N	REV.	SPL. LOT NO.		
PART NAME POWER CHO (ROHS+H.		PART NO. CSMS0410D-XXXX-LRH	REV.	DATE OF ISSUE	Q'TY 0	PCS
]	ENG	INEERING CHAN	GE NO	TICE - REC	ORD	
REVISION NO.		<b>REVISION DESCRIPTIO</b>	DN	AUTHOR	DATE	REMARI
		COPURED OF THE PASSING STATE	很高。 股份有限 SAA stem Alliance electrics RICS CO., IT	WISHSSERVED. THE THE THE		



#### 5. Electrical Characteristics:

Part number Inductance Ind Symbol		Nominal Inductance	Inductance	DC Resistance	Rated Current (mA)		Self-resonant Frequency
	(uH) Tolerance @100KHz		(Ω) ±20%	Saturation Current Idc1	Temperature Rise Current Idc2	(MHz) Min.	
CSMS0410D-1R0N-LRH	Α	1.0	±30%	0.056	2000	1900	116
CSMS0410D-2R2M-LRH	С	2.2	±20%	0.085	1200	1500	73
CSMS0410D-3R3M-LRH	E	3.3	±20%	0.100	1100	1400	58
CSMS0410D-4R7M-LRH	н	4.7	±20%	0.140	950	1200	47
CSMS0410D-6R8M-LRH	I	6.8	±20%	0.200	800	1000	38
CSMS0410D-100M-LRH	К	10	±20%	0.300	620	750	31
CSMS0410D-150M-LRH	м	15	<b>±20%</b>	0.430	540	600	24
CSMS0410D-220M-LRH	N	22	<b>±20%</b>	0.570	450	500	19

1. Test Frequency: 100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent. DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current Idc2: The value of current causes a 40°C temperature rise.

5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

- 6. Operating Temperature Range:-25°C to +125°C (Including self-temperature rise)
- 7. Storage Temp. Range : -40°C to +85°C
- 8. MSL : Level 1

#### 6. Structural Drawing:

(Magnetic Shielded Type)

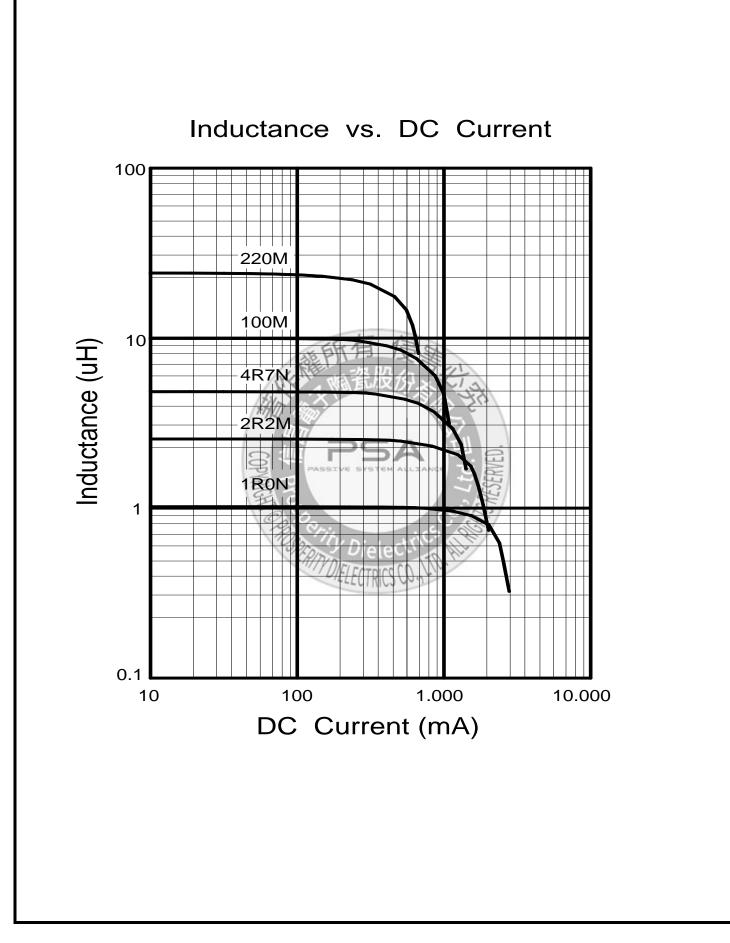
1	Ferr	ite c	ore.	١

- Ni-Zn ferrite
- 2 Winding wire
- ③ Over-coating resin.
- ④ Electrode

Polyurethane-copper wire Epoxy resin, containing ferrite powder External electrode (substrate) External electrode (base plating) External electrode (top surface solder coating)

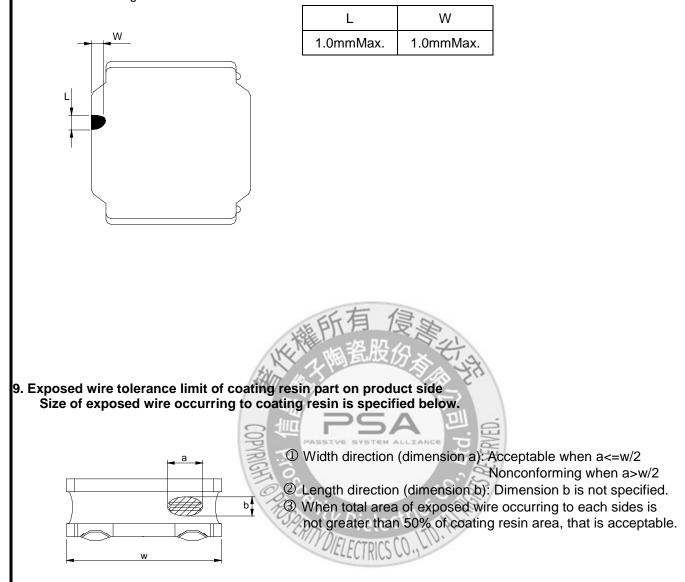
Ag Ni-Sn Sn-Ag-Cu

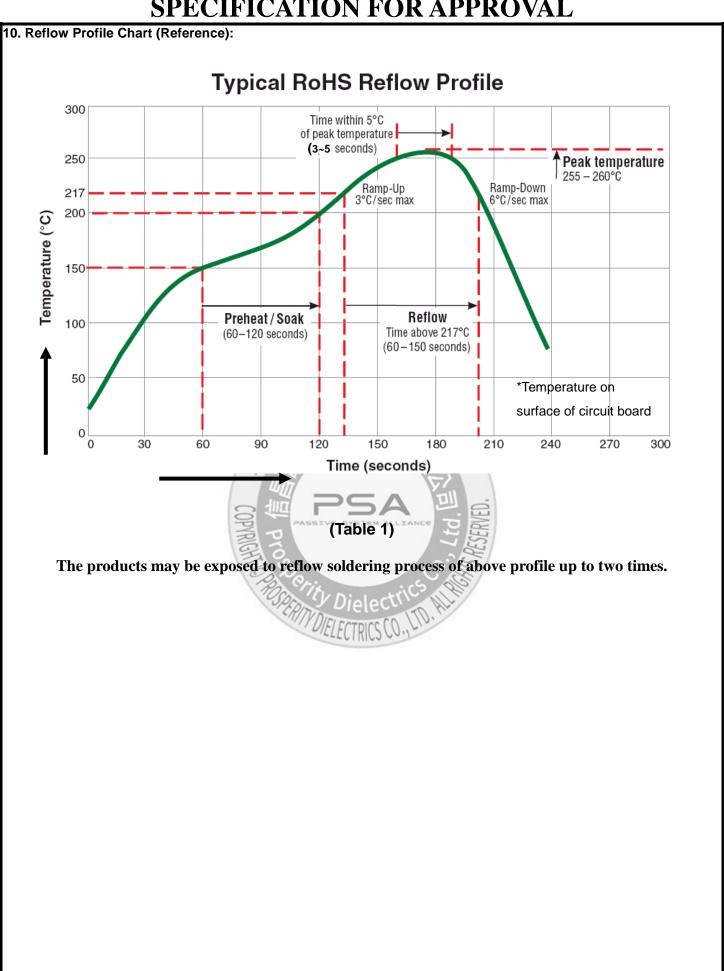
7. Electrical Curve :



#### 8. Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension





	Test Item	Standard	Test method
Resistance to Deflection		No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			10 Force R230 Rod
STICS			$R5 \longrightarrow Board \\ Sample \\ 45\pm2 \\ 45\pm2 \\ 45\pm2 \\ 45\pm2 \\ 6.8 \\ 1.4 \\ 0.8 \\ 1.4 \\ 0.8 \\ 0.8 \\ 1.4 \\ 0.8 \\ 0.$
ERIG			Land dimensions
ACT			Test board size :100×40×10 Test board material I: glass epoxy-resin
A R/			Solder cream thickness:0.1 Unit: mr
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1. 10  N, 5  s
ME		COPYRIGH PASSE	Applied force:10 N to X and Y directions Duration:5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s
			0.6W

Test Item	Standard		Test method	
Resistance to Vibration	△L/L:within±10%      No abnormality	The test samples shall The reflow soldering co It shall be submitted to	Table 1.Then	
	observed	Frequency range 10H	Hz~55Hz	
	In appearance	196	mm(May not excee ፩ m/S²)	
		Sweeping Method 10H		
		Time For	2 hours on each X	,Y, and Z axis.
Resistance to	△L/L:within±10%	The test sample shall b 230±5 deg C for 40 sec	conds, with peak te	
Soldering heat	No abnormality	260±5 deg C for 5 sec	conds, 2 times.	
(Reflow)	observed In appearance	Test board thickness:1. Test board material :gla		
Solder ability	At least 90% of surface	The test samples shall Immersed in molten so		
	of terminal electrode is	Flux: Methanol solution		
	covered by new solder.	Solder Temperature	245±deg C	
		the Time	5±1.0 S.	
	大道 一	Immersing Speed	25 mm/s	
Temperature Characteristics	△L/L:within±20% No abnormality observed In appearance	Measurement of induct Range within -25 deg C With reference to induc Rate shall be calculated	C to +85 deg C. Stance value at +20	
Thermal shock	△L/L:within±10% No abnormality observed In appearance	The test samples shall By the reflow soldering The test samples shall Shown in below table in The temperature cycle	conditions shown i be placed at specif n sequence.	n Table 1. ied
	Posti	Conditions of steps for	1 cycle	
	SPERIN	Step Temperatu	ire Time(	min)
		1 -40±3 deg	C 30±	-3
		2 Room Terr	np 3 maxi	mum
		3 85±2 deg		
		4 Room Tem	np 3 maxi	mum
Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test Conditions as shown in below table.		Table 1.
		Temperature	-40±2 deg C	
		Time	500 +24/-0 h	

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	Test Item	Standard	Test method
	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.
			Temperature 85±2 deg C
			Applied current (Refer to Page 2)
			Time 500+24/-0 h
TESTS	Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table.
ENT			Temperature 60±2 deg C
MN		Late F	Humidity 90~95%RH Time 500+24/-0 h
ENVIRONMENT TESTS		AND IS IS	周瓷股份查查计制
	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Applied current Rated current (Refer to Page 2)) Time 500+24/-0 h

