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DESCRIPTION	
SAMPLE LOT	'NO.
PART NO.	CSMS0412D-XXXX-LRH
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Once you approve this part, please	sign and return this page to the following marked location.
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Customer Signature:	Date:
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This part currently development section.	□Production line can produce this series of products.
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Sales Office-Headquarter	DIELE Yong Zhou Plant
No. 566-1, Kao-Shi Rd., Yangmei, Tao	yuan 32668, The Tao-Yuan Rd., Fenghuang Park, Lengshuitan
Taiwan	District, Yongzhou, Hunan 425000, P.R.C.
TEL: +886-3-475-3355	TEL: +86-746-8610-180
FAX: +886-3-485-4959	FAX: +86-746-8610-181
Sales Office-Dong Guan,China	

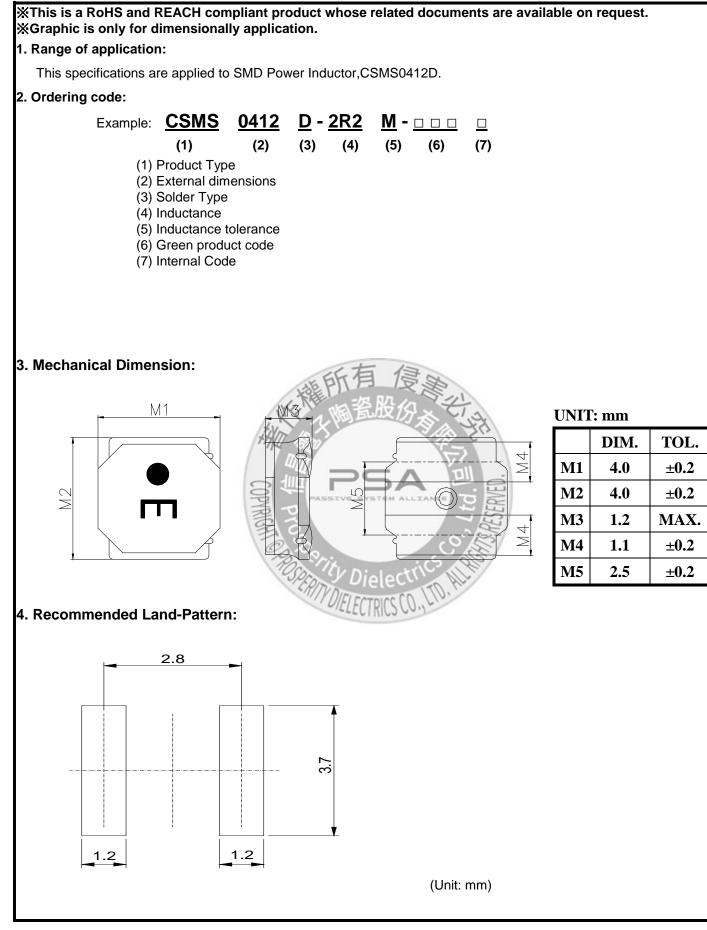
Sales Office-Dong Guan,Cinna No.638,Mei Jing West Road Xiniupo Administrative Zone Dalang Town,Dong Guan City,GuangDong Province,China. TEL: +86-769-8555-0979 FAX: +86-769-8555-0972

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CUSTOMER	CUSTOMER P/N	REV.	SPL. LOT NO.		
PART NAME POWER CHOKE (RoHS+H.F.)			DATE OF ISSUE		PCS
REVISION NO.	GINEERING CHAI		AUTHOR	ORD DATE	REMAR
	COPPOSITE LAND	与 侵害 品股份有限 SAA by STEM ALLIANCE ielectrics CTRICS CO., ITD	CHISTER CHISTER		



5. Electrical Characteristics:

	Inductance Nominal		Inductance	DC Resistance	Rated Current (mA)		
Part number	Symbol	Inductance (uH) Tolerance		(Ω) ±20%	Saturation Current Idc1	Temperature Rise Current Idc2	Self-resonant Frequency Min (MHz)
CSMS0412D-1R0N-LRH	Α	1.0	±30%	0.042	2800	2200	100
CSMS0412D-2R2M-LRH	С	2.2	±20%	0.060	1650	1900	70
CSMS0412D-3R3M-LRH	E	3.3	±20%	0.070	1400	1700	60
CSMS0412D-4R7M-LRH	Н	4.7	±20%	0.095	1200	1500	45
CSMS0412D-6R8M-LRH	I	6.8	±20%	0.125	900	1300	35
CSMS0412D-100M-LRH	К	10	±20%	0.170	800	1100	30
CSMS0412D-150M-LRH	м	15	±20%	0.260	650	750	24
CSMS0412D-220M-LRH	N	22	±20%	0.400	500	620	18

1. Test Frequency: 100KHz

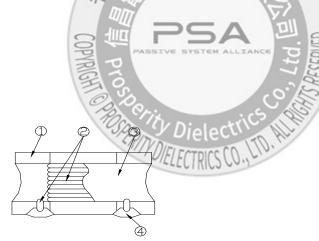
2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent. DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

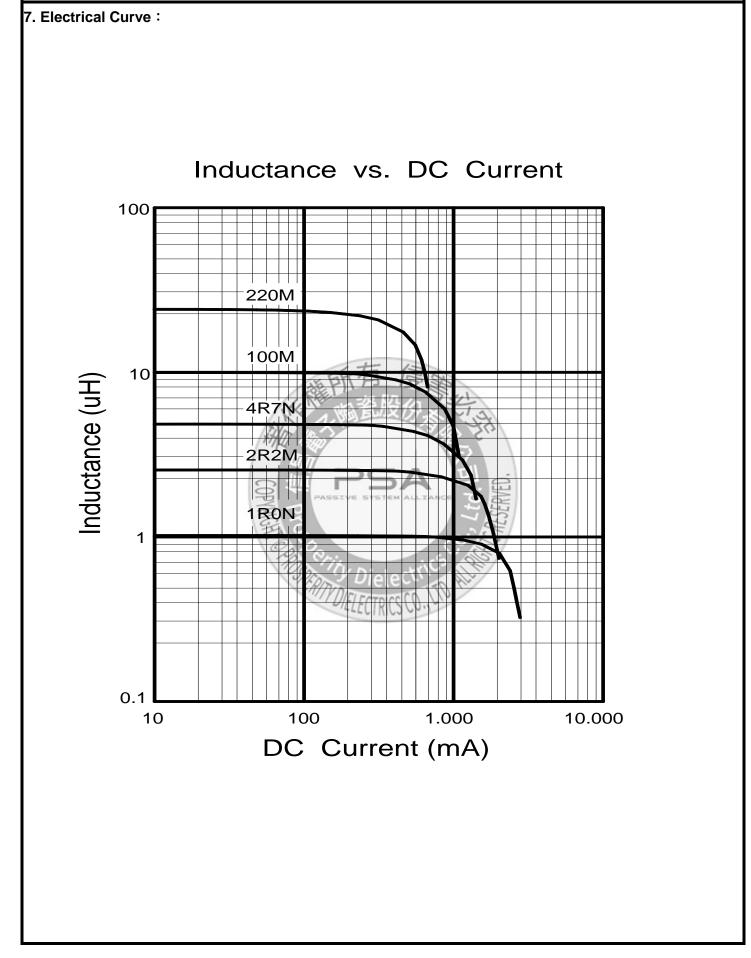
- 4. Temperature rise current ldc2: The value of current causes a 40 $^\circ\!\mathrm{C}$ temperature rise.
- 5. Rated Current: Either Idc1 or Idc2 whichever is smaller.
- 6. Operating Temperature Range:-25°C to +125°C (Including self-temperature rise)
- 7. Storage Temp. Range : -40°C to +85°C
- 8. MSL : Level 1

6. Structural Drawing:



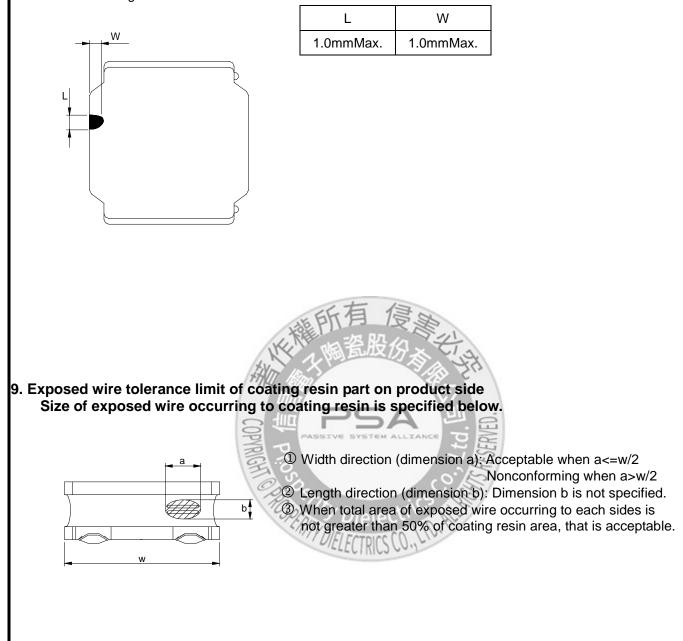
(Magnetic Shielded Type)

1) Ferrite core.	Ni-Zn ferrite	
② Winding wire	Polyurethane-copper wire	
③ Over-coating resin.	Epoxy resin, containing ferrite powder	
④ Electrode	External electrode (substrate)	Ag
	External electrode (base plating)	Ni-Sn
	External electrode (top surface solder coating)	Sn-Ag-Cu

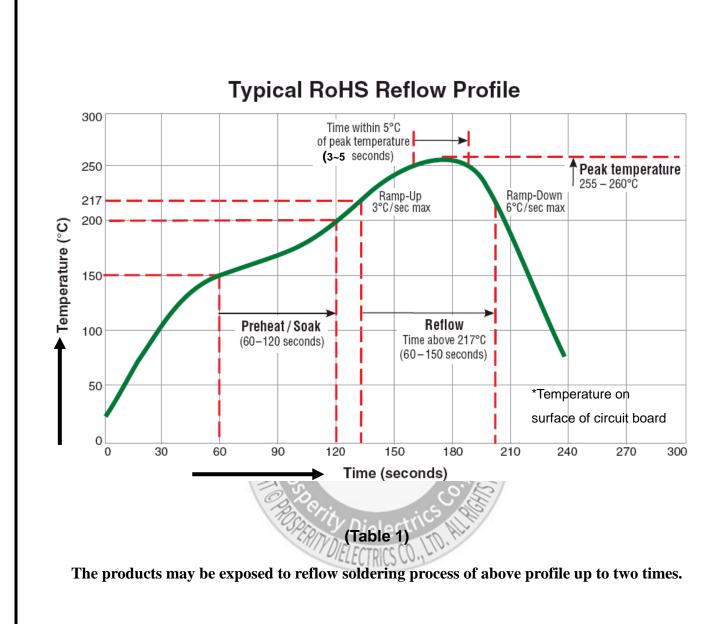


8.Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension



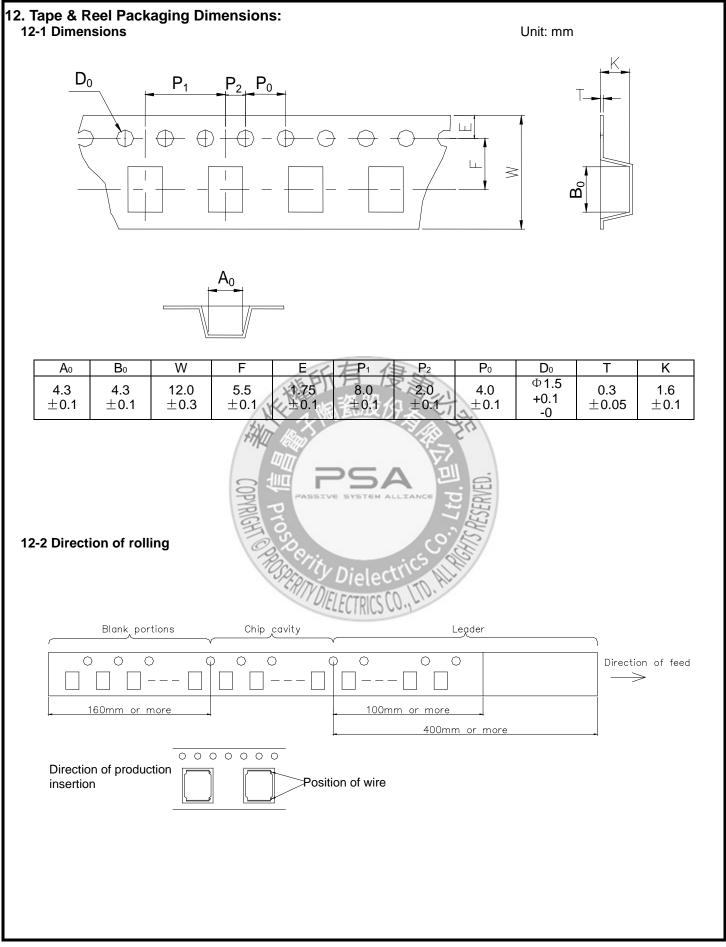
10. Reflow Profile Chart (Reference):



Test Item Resistance to Deflection	Standard No damage.	Test method The test samples shall be soldered to the test board
	No damage.	
		by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
		Torce R230 Rod R230
		R5
		Land dimensions
		Test board size :100×40×10 Test board material I: glass epoxy-resin
		Solder cream thickness:0.1 Unit: mn
Terminal Electrode	board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.
Body strength	No damage	Applied force :20 N Duration :10 s
	Electrode	Terminal Electrode

Vibration No abnormality It shall be submitted to below test conditions Vibration No abnormality It shall be submitted to below test conditions Vibration Frequency range 10Hz~55Hz	Test Item	Standard		Test method	
In appearance In appearance In appearance In appearance Resistance to △L/L:within±10% The test sample shall be exposed to reflow oven at 230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times. Solder ability At least 90% of surface of terminal electrode is covered by new solder. The test samples shall be dipped in flux, and then immersed in molten solder as shown in below table. Flux: Methanol solution containing rosin 25%. Solder ability At least 90% of surface of terminal electrode is covered by new solder. Solder Temperature 245±deg C Temperature △L/L:within±20% No abnormality observed in appearance Solder Temperature 245±deg C Temperature △L/L:within±10% No abnormality observed in appearance Measurement of inductance value at +20 deg C, chan Range within 25 deg C to +85 deg C. Thermal shock △L/L:within±10% No abnormality observed in appearance The test samples shall be calculated. Thermal shock △L/L:within±10% No abnormality observed in appearance The test samples shall be calculated. The test samples shall be placed at specified Shown in below table. The test samples shall be placed at specified Shown in Table 1. The test samples shall be calculated. The test samples shall be placed at specified Shown in action soldering conditions shown in Table 1. The test samples shall be placed at test board by the reflow soldering conditions			The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then It shall be submitted to below test conditions		
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Image: Time For 2 hours on each X, Y, and Z axi Resistance to △L/L:within±10% The test sample shall be exposed to reflow oven at 230±5 deg C for 40 seconds, 2 times. Soldering heat No abnormality observed Test board material :glass epoxy-resin Solder ability At least 90% of surface of terminal electrode is covered by new solder. The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table. Flux: Methanol solution containing rosin 25% Solder ability At least 90% of surface of terminal electrode is covered by new solder. The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table. Flux: Methanol solution containing rosin 25% Solder ability At least 90% of surface of terminal electrode is covered by new solder. Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. Temperature Characteristics △L/L:within±10% Measurement of inductance value at +20 deg C, with reference to inductance value at +20 deg C, chan Rate shall be calculated. Thermal shock △L/L:within±10% No abnormality observed In appearance The test samples shall be placed to test board By the reflow soldering conditions shown in Table 1. The test samples shall be calculated. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle					
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(Reflow) observed In appearance Test board thickness:1.0 mm Test board material :glass epoxy-resin Solder ability At least 90% of surface of terminal electrode is covered by new solder. The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table. Flux: Methanol solution containing rosin 25% Solder Temperature Characteristics △L/L:within±20% No abnormality observed In appearance Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, chan Rate shall be calculated. Thermal shock △L/L:within±10% No abnormality observed In appearance The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature 1 4 40±3 deg C 30±3 2 800m Temp Low Temperature life Test △L/L:within±10% No abnormality observed The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test	Resistance to	△L/L:within±10%	230±5 deg C for 40	seconds, with peak temperature at	
Solder ability At least 90% of surface of terminal electrode is covered by new solder. The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table. Flux: Methanol solution containing rosin 25% Temperature Characteristics $\triangle L/L$:within±20% No abnormality observed Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, chan Rate shall be calculated. Thermal shock $\triangle L/L$:within±10% No abnormality observed In appearance The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature Time(min) 1 4 -40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 4 Room Temp 3 maximum Low Temperature life Test $\triangle L/L$:within±10% No abnormality observed The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature Time(min) 1 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum	Soldering heat	No abnormality	260±5 deg C for 5	seconds, 2 times.	
Solder ability At least 90% of surface of terminal electrode is covered by new solder. The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table. Temperature Characteristics $\triangle L/L$:within±20% Solder Temperature 245±deg C Temperature Characteristics $\triangle L/L$:within±20% Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. Thermal shock $\triangle L/L$:within±10% Measurement of inductance shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature Time(min) 1 -40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 Ast±2 deg C 30±3 4 Room Temp 3 maximum 3 Ast±2 deg C 30±3 </td <td>(Reflow)</td> <td>observed</td> <td>Test board thicknes</td> <td>s:1.0 mm</td>	(Reflow)	observed	Test board thicknes	s:1.0 mm	
Description At least 50.% of sufface of terminal electrode is for terminal electrode is covered by new solder. Solder Temperature Temperature 245±deg C Characteristics L/L:within±20% No abnormality Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, chan Rate shall be calculated. The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The test samples shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Step Temperature Time(min) 1 40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 <		In appearance	Test board material	:glass epoxy-resin	
covered by new solder. Solder Temperature 245±deg C Time 5±1.0 S. Immersing Speed 25 mm/s Characteristics \[\lambda L/L:within±20%] No abnormality observed In appearance Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, chan Rate shall be calculated. Thermal shock \[\Delta L/L:within±10%] No abnormality observed In appearance The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The test samples thall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature Time(min) 1 40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 7 The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test	Solder ability		Immersed in molter	n solder as shown in below table.	
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Immersing Speed 25 mm/s Temperature Characteristics △L/L:within±20% No abnormality observed In appearance Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, chan Rate shall be calculated. Thermal shock △L/L:within±10% No abnormality observed In appearance The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature Time(min) 1 2 Room Temp 3 maximum 3 85±2 deg C 30±3 2 30±3 4 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 4 Room Temp 3 maximum		covered by new solder.			
Temperature Characteristics △L/L:within±20% No abnormality observed In appearance Measurement of inductance shall be taken at temperat Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, chan Rate shall be calculated. Thermal shock △L/L:within±10% No abnormality observed In appearance The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature Time(min) 1 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 4 Room Temp 3 maximum		145 F	ME ES		
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Thermal shock △L/L:within±10% The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle Step Temperature Time(min) 1 -40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum Add Room Temp 3 maximum Add Room Temp 3 maximum		No abnormality observed	Range within -25 de With reference to in	eg C to +85 deg C. ductance value at +20 deg C, change	
Step Temperature Time(min) 1 -40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum Composition 3 maximum 3 maximum Low The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test	Thermal shock	△L/L:within±10% No abnormality observed	By the reflow solder The test samples sl Shown in below tab	ring conditions shown in Table 1. hall be placed at specified le in sequence.	
Step Temperature Time(min) 1 -40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum Composition 3 maximum 3 maximum Low The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test		Posti	Conditions of steps	for 1 cycle	
Low AL/L:within±10% Temperature life AL/L:within±10% Test AL/L:within±10% The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test		ERITY			
Image: Second		112			
Low A Room Temp 3 maximum Low A The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. Test After that, the test samples shall be placed at test			2 Room	Temp 3 maximum	
Low \[\L/L:within±10% Temperature life Test The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test					
Temperature life TestNo abnormality observedThe reflow soldering conditions shown in Table 1.After that, the test samples shall be placed at test			4 Room	Temp 3 maximum	
	Temperature life	No abnormality observed	The reflow soldering After that, the test s	g conditions shown in Table 1. amples shall be placed at test	
Temperature -40±2 deg C			Temperature	-40±2 deg C	
Time 500 +24/-0 h			Time	500 +24/-0 h	

		Oten last	-	and the set	
	Test Item	Standard	Test method		
	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	soldering conditions shown in The test samples shall be plac		
			Temperature	85±2 deg C	
			Applied current	Rated current (Refer to Page 2)	
			Time	500+24/-0 h	
ESTS	Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be sold soldering conditions shown in The test samples shall be plac specified temperature and hun	ed in thermostatic oven set at	
			Temperature	60±2 deg C	
JEN			后有 月 Humidity	90~95%RH	
NN		+ # E	Time	500+24/-0 h	
ENVIRONMENT TESTS		HE MA	周省版历查和学科		
	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	soldering conditions shown in The test samples shall be plac	ed in thermostatic oven set at nidity and applied the rated current	
		0,00	Temperature	60±2 deg C	
		POST	Humidity	90~95%RH	
		CERITY	ELECTRICS Applied current	Rated current (Refer to Page 2))	
			Time	500+24/-0 h	



12-3 Reel

