

TEL: +86-769-8555-0979 FAX: +86-769-8555-0972

## SPECIFICATION FOR APPROVAL

DATE	
DOC. REV.	
PART NO.	CSMS0510D-XXXX-LRH
SAMPLE LOT NO.	
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
CUST. DOC. REV.	
CUST. PART NO.	
CUSTOMER	

Once you approve this part, please sign and return this page to the following marked location.

Customer Signature:	5A Date:
This part currently development section.	Production line can produce this series of products
Sales Office-Headquarter No. 566-1, Kao-Shi Rd., Yangmei, Taoyuan 32668, Taiwan TEL: +886-3-475-3355 FAX: +886-3-485-4959	Yong Zhou Plant Tao-Yuan Rd., Fenghuang Park, Lengshuitan District, Yongzhou, Hunan 425000, P.R.C. TEL: +86-746-8610-180 FAX: +86-746-8610-181
Sales Office-Dong Guan, China No.638, Mei Jing West Road Xiniupo Administrative Zone Dalang Town, Dong Guan City, Guang Dong Province China	

TESTED BY	CHECKED BY	APPROVED BY

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CUSTOMER	CUSTOMER P/N	REV.	SPL. LOT NO.		
	3 5 5 5 5 5 5 7 7 1	_			
ART NAME POWER CHOKE (ROHS+H.F.)	PART NO. CSMS0510D-XXXX-LR	REV.	DATE OF ISSUE	Q'TY	PCS
EN	GINEERING CHA	NGE NO	TICE - REC	CORD	
REVISION NO.	REVISION DESCRIPT	ION	AUTHOR	DATE	REMAR
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	POSPERITY DIFTE	Pielectrics CTRICS CO., LTD			
	, LLL	CIMICOCON			

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※This is a RoHS and REACH compliant product whose related documents are available on request.

**※Graphic is only for dimensionally application.** 

### 1. Range of application:

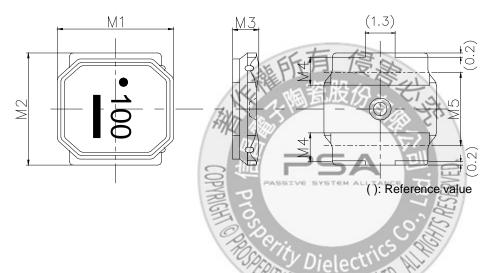
This specifications are applied to SMD Power Inductor, CSMS0510D.

#### 2. Ordering code:

Example:  $\frac{CSMS}{(1)}$   $\frac{0510}{(2)}$   $\frac{D}{(3)}$   $\frac{M}{(4)}$   $\frac{D}{(5)}$   $\frac{D}{(6)}$   $\frac{D}{(7)}$ 

- (1) Product Type
- (2) External dimensions
- (3) Solder Type
- (4) Inductance
- (5) Inductance tolerance
- (6) Green product code
- (7) Internal Code

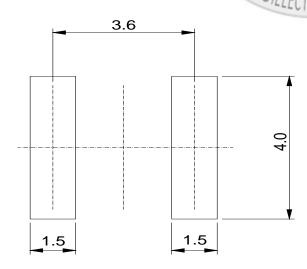
#### 3. Mechanical Dimension:



**UNIT: mm** 

	DIM.	TOL.
M1	4.9	±0.2
M2	4.9	±0.2
М3	1.0	MAX.
M4	1.2	±0.2
M5	3.3	±0.2

4. Recommended Land-Pattern:



(Unit: mm)

#### 5. Electrical Characteristics:

Nominal Part number Inductance (uH)	Inductance	Inductance	DC Resistance	Rated Current (mA)		
	Tolerance	(Ω) ±20%	Saturation Current Idc1	Temperature Rise Current Idc2	Self-resonant Frequency Min (MHz)	
CSMS0510D-1R0N-LRH	1.0	±30%	0.070	2350	1750	95
CSMS0510D-2R2N-LRH	2.2	±30%	0.105	1500	1400	65
CSMS0510D-3R3M-LRH	3.3	±20%	0.125	1400	1250	42
CSMS0510D-4R7M-LRH	4.7	±20%	0.145	1200	1150	37
CSMS0510D-6R8M-LRH	6.8	±20%	0.185	1000	1000	33
CSMS0510D-100M-LRH	10	±20%	0.250	850	900	23
CSMS0510D-150M-LRH	15	±20%	0.400	680	650	19
CSMS0510D-220M-LRH	22	±20%	0.600	550	450	15

1. Test Frequency: 100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent.

DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current Idc2: The value of current causes a 40°C temperature rise.

5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

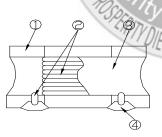
6. Operating Temperature Range:-25°C to +125°C (Including self-temperature rise)

7. Storage Temp. Range: -40°C to +85°C

8. MSL: Level 1

PSA
PASSIVE SYSTEM ALLIANCE

#### 6. Structural Drawing:



(Magnetic Shielded Type)

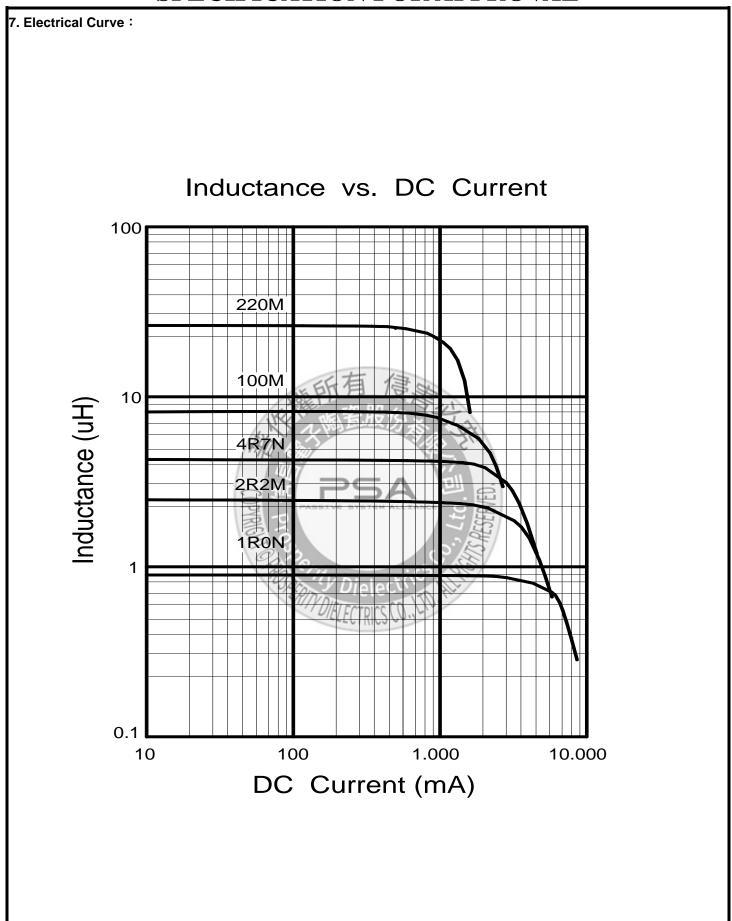
① Ferrite core. Ni-Zn ferrite

② Winding wire Polyurethane-copper wire

③ Over-coating resin. Epoxy resin, containing ferrite powder

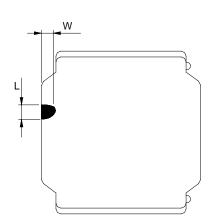
④ ElectrodeExternal electrode (substrate)Ag

External electrode (base plating) Ni-Sn
External electrode (top surface solder coating) Sn-Ag-Cu



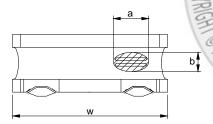
### 8. Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension

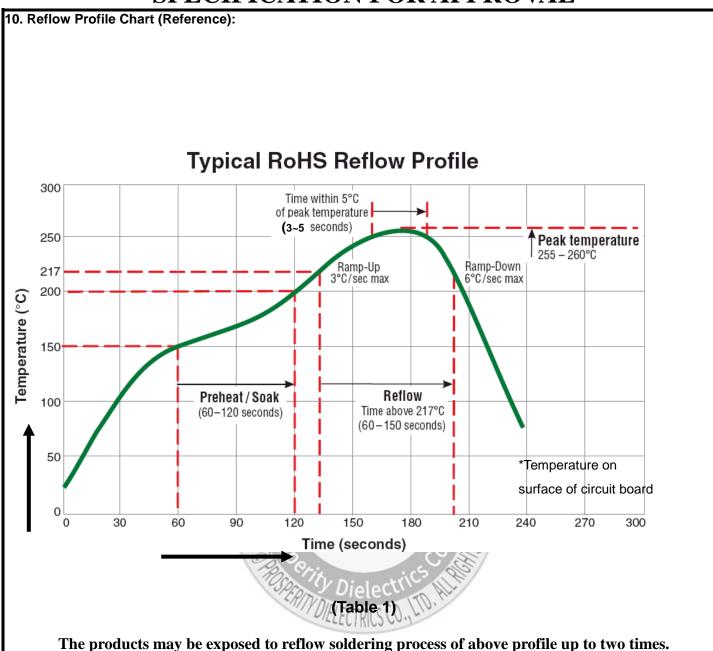


L	W	
1.5mmMax.	1.5mmMax.	

Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
- When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

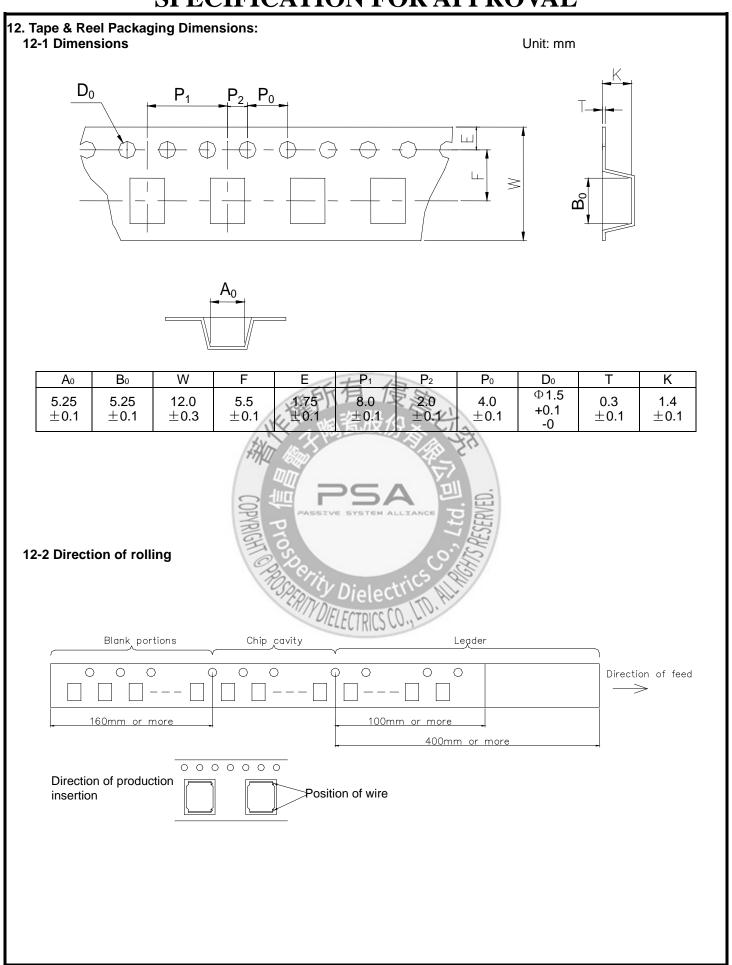


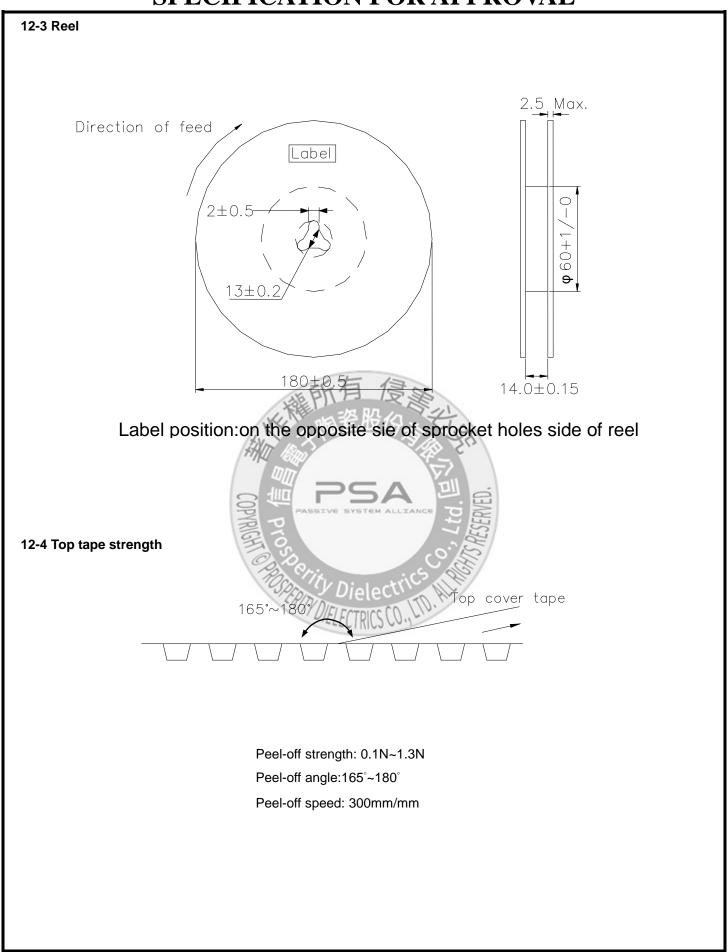
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Мес	hanical Performan	ce /Environmental Test Pe	erformance Specifications:
	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			Force Rod R230
STICS			R5 Board Test Sample 45±2 45±2 0.8 1.4 0.8
ERI			Land dimensions Test board size :100×40×10
AC			Test board material I: glass epoxy-resin
AR	A 11 .	0 " " " " " "	Solder cream thickness:0.1 Unit: mm
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.  The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  Applied force: 10 N to X and Y directions
		COPYRIGHT Pro	Duration:5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s  R0.5mm Sample
			0.6W

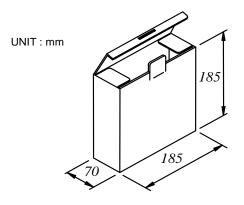
Test Item	Standard		Test method
Resistance to	△L/L:within±10%	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then	
Vibration	No abnormality	It shall be submitted	to below test conditions
	observed	Frequency range	
	In appearance	Total Amplitude	I.5mm(May not exceed acceleration 196 m/S²)
		Sweeping Method 1	10Hz to 55Hz to 10 Hz for 1 min.
		Time F	For 2 hours on each X,Y, and Z axis
Resistance to	△L/L:within±10%		Il be exposed to reflow oven at seconds, with peak temperature at
Soldering heat	No abnormality	260±5 deg C for 5 s	
(Reflow)	observed	Test board thickness	
	In appearance	Test board material:	glass epoxy-resin
Solder ability	At least 90% of surface	The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table.	
	of terminal electrode is	Flux: Methanol solut	ion containing rosin 25%
	covered by new solder.	Solder Temperature	e 245±deg C
		力 Time	5±1.0 S.
	大道	Immersing Speed	25 mm/s
Temperature	△L/L:within±20%	Measurement of inductance shall be taken at temperatu Range within -25 deg C to +85 deg C.	
Characteristics	No abnormality		g C to +85 deg C. luctance value at +20 deg C, chang
	observed In appearance	Rate shall be calcula	
Thermal shock	△L/L:within±10%	The test samples sh	all be soldered to test board
	No abnormality	By the reflow soldering conditions shown in Table 1.	
	observed	The test samples sh Shown in below table	all be placed at specified
	In appearance		cle shall be repeated 100 cycles.
	C Po Cori	Conditions of steps f	The state of the s
	Spion	J DIAIACU AND	7/
	-11/1/	Step Temper	` '
		2 Room T	
		3 85±2 de	eg C 30±3
		4 Room T	emp 3 maximum
Law	↑ 1 /1ithin : 4.00/	The test growning of	
Low Temperature life	△L/L:within±10% No abnormality		all be soldered to the test board by conditions shown in Table 1.
Test	observed	After that, the test sa	amples shall be placed at test
	In appearance	Conditions as shown	
		Temperature	-40±2 deg C
		Time	500 +24/-0 h

	1		
	Test Item	Standard	Test method
	Loading at high temperature life test	△L/L:within±10%  No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.
			Temperature 85±2 deg C  Applied current (Refer to Page 2)  Time 500+24/-0 h
ENVIRONMENT TESTS	Damp heat life test	△L/L:within±10%  No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table.  Temperature 60±2 deg C Humidity 90~95%RH Time 500+24/-0 h
	Loading under Damp heat life test	△L/L:within±10%  No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table.  Temperature 60±2 deg C Humidity 90~95%RH  Applied current (Refer to Page 2))  Time 500+24/-0 h





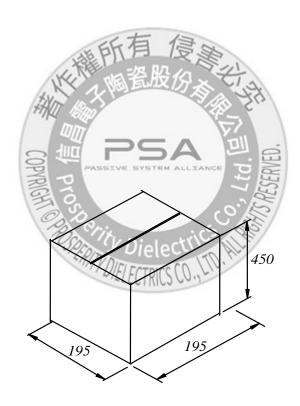
### 12-5 Dimensions of packing box (for Tape & Reel package)



CONSTURCTION:

THE CASE CONTAINS 4-12mm WIDE CARRIER TAPES.

Q'TY: 1,000/ REEL



TOTAL Q'TY : 24,000 PCS