CUSTOMER	
CUST. PART NO.	
CUST. DOC. REV.	
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
SAMPLE LOT NO.	
PART NO.	CSMS0620D-XXXX-LRH
DOC. REV.	
DATE	

Once you approve this part, please sign and return this page to the following marked location.

Customer Signature :	S #			Date:	
•	-0	-WRRTAG BIRLEM	ALLIANCE 6	0-	

This part currently development section.

Production line can produce this series of products.

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CUSTOMER	CUSTOMER P/N	REV.	SPL. LOT NO.		
PART NAME POWER CHOKI (ROHS+H.F.)	PART NO. CSMS0620D-XXXX-LRH	REV.	DATE OF ISSUE	Q'TY 0	PCS
EN	GINEERING CHAN	GE NO	TICE - REC	ORD	
REVISION NO.	REVISION DESCRIPTIO	N	AUTHOR	DATE	REMARK
	槌所有	侵害			
	州加州	股份有			
	COPYRIGHTOS PASSETVE SYS	TEM ALLIANCE	SAESERVED.		
	PROPERTY DIE	lectrics RCO,LT			

P2 Rev.B

※This is a RoHS and REACH compliant product whose related documents are available on request.

XGraphic is only for dimensionally application.

1. Range of application:

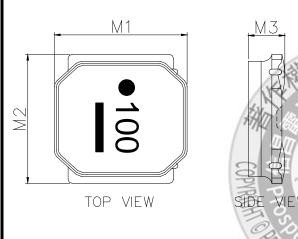
This specifications are applied to SMD Power Inductor, CSMS0620D.

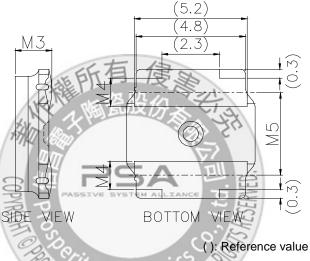
2. Ordering code:

Example: $\frac{CSMS}{(1)}$ $\frac{0620}{(2)}$ $\frac{D-2R2}{(3)}$ $\frac{M}{(4)}$ $\frac{\Box}{(5)}$ $\frac{\Box}{(6)}$

- (1) Product Type
- (2) External dimensions
- (3) Solder Type
- (4) Inductance
- (5) Inductance tolerance
- (6) Green product code
- (7) Internal Code

3. Mechanical Dimension:

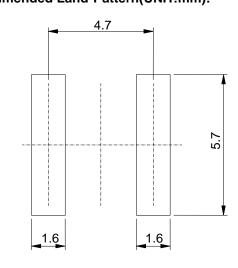




UNIT: mm

	DIM.	TOL.
M1	6.0	±0.2
M2	6.0	±0.2
М3	2.0	MAX.
M4	1.35	±0.2
М5	4.0	±0.2

4. Recommended Land-Pattern(UNIT:mm):



5. Electrical Characteristics:

Part number	Nominal Inductance	Inductance	DC Resistance	Cur	ted rent ıA)	Self-resonant Frequency
Fait number	(uH) @100KHz	Tolerance.	(Ω) ±20%	Saturation Current Idc1	Temperature Rise Current Idc2	Min (MHz)
CSMS0620D-R80N-LRH	0.8	±30%	0.020	6400	4100	110
CSMS0620D-1R5N-LRH	1.5	±30%	0.026	4300	3600	93
CSMS0620D-2R2N-LRH	2.2	±30%	0.034	3200	2900	73
CSMS0620D-3R3N-LRH	3.3	±30%	0.040	2800	2750	55
CSMS0620D-4R7N-LRH	4.7	±30%	0.058	2400	2150	43
CSMS0620D-6R8N-LRH	6.8	±30%	0.085	2000	1800	30
		20%	0.125			
CSMS0620D-220M-LRH	22	±20%	0.290	1250	950	11

1. Test Frequency: 100KHz

2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent.

DCR: Chroma16502 or equivalent. SRF: HP4291B or equivalent.

3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.

4. Temperature rise current ldc2: The value of current causes a 40°C temperature rise.

5. Rated Current: Either Idc1 or Idc2 whichever is smaller.

6. Operating Temperature Range:-25°C to +130°C (Including self-temperature rise)

7. Storage Temp. Range: -40°C to +85°C

8. MSL: Level 1

6. Structural Drawing:



(Magnetic Shielded Type)

① Ferrite core. Ni-Zn ferrite

② Winding wire Polyurethane-copper wire

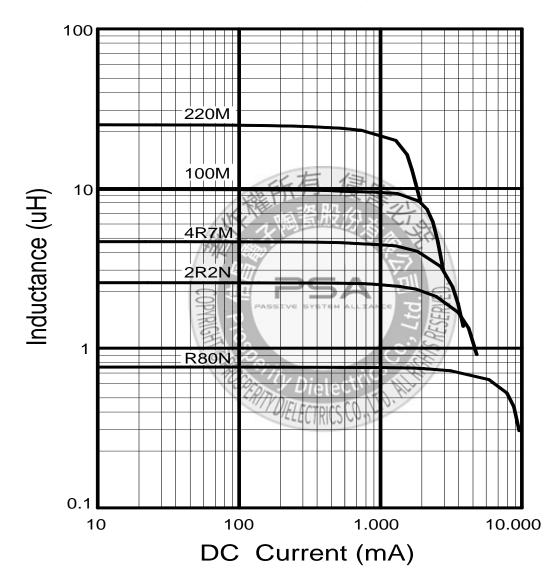
③ Over-coating resin.
Epoxy resin, containing ferrite powder

④ ElectrodeExternal electrode (substrate)

External electrode (base plating) Ni-Sn
External electrode (top surface solder coating) Sn-Ag-Cu

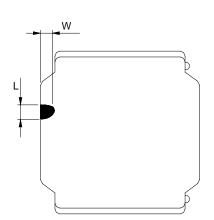


Inductance vs. DC Current



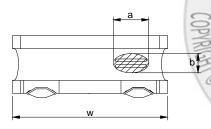
8. Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension

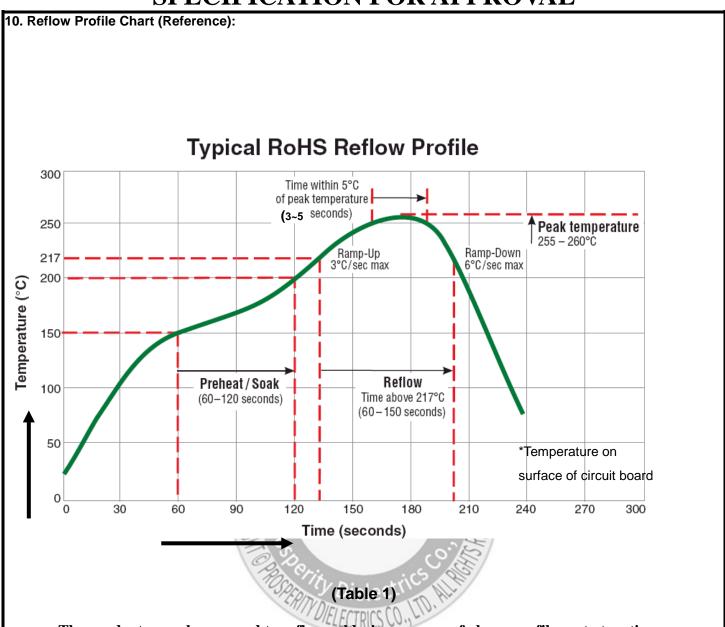


L	W	
1.5mmMax.	1.5mmMax.	

 Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2
 Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
- ③ When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

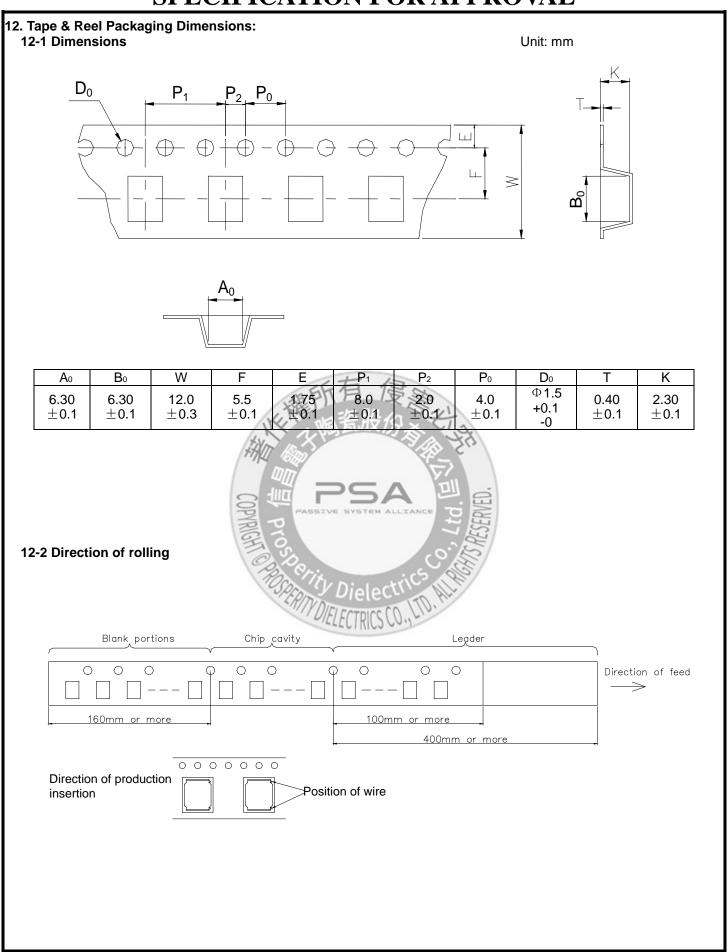


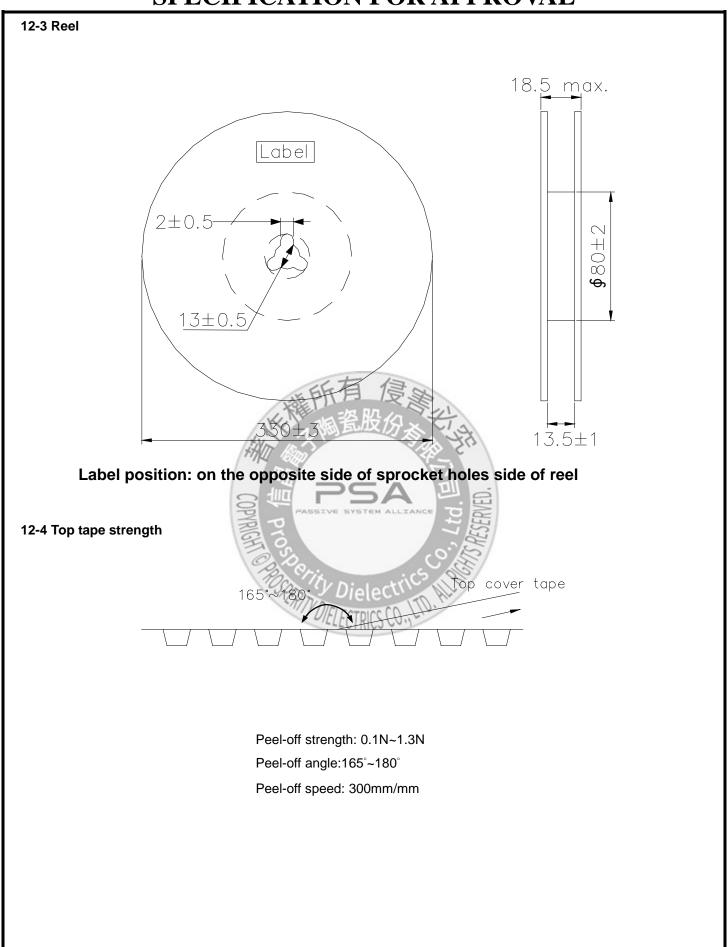
The products may be exposed to reflow soldering process of above profile up to two times.

Мес	hanical Performan	ce /Environmental Test Pe	errormance Specifications:
	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			Force R230
STICS			R5 — Board Ci Ci Ci Ci Ci Ci Ci C
ERIS			Land dimensions
٩CT			Test board size :100×40×10 Test board material I: glass epoxy-resin
AR/			Solder cream thickness:0.1 Unit: mm
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.
M		COPYRIGHT PROSE	Applied force:10 N to X and Y directions Duration:5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s Ro.5mm —Sample
			0.6W

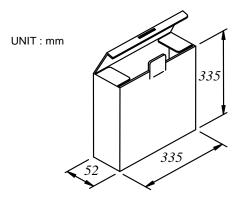
Test Item	Standard		Test method	
Resistance to	△L/L:within±10%	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then		
Vibration	No abnormality	It shall be submitted		
	observed	Frequency range 1		
	In appearance		.5mm(May not exce 96 m/S ²)	ed acceleration
		Sweeping Method 1		Iz for 1 min.
		Time F	or 2 hours on each	X,Y, and Z axis.
Resistance to	△L/L:within±10%	The test sample shal		
Soldering heat	No abnormality	230±5 deg C for 40 s 260±5 deg C for 5 s		emperature at
(Reflow)	observed	Test board thickness:	:1.0 mm	
	In appearance	Test board material :		
Solder ability	At least 90% of surface	The test samples sha Immersed in molten s		
	of terminal electrode is	Flux: Methanol solution		
	covered by new solder.	Solder Temperature	245±deg C	
	1.6	有Time	5±1.0 S.	
	持是	Immersing Speed	25 mm/s	
Temperature Characteristics	△L/L:within±20%	Measurement of inductance shall be taken at temperatur Range within -25 deg C to +85 deg C.		en at temperature
	No abnormality observed	With reference to ind	uctance value at +2	0 deg C, change
	In appearance	Rate shall be calcula		
Thermal shock	△L/L:within±10%	The test samples sha By the reflow soldering		
	No abnormality	The test samples sha		
	observed In appearance	Shown in below table		400
	0,00	The temperature cyc	ie shaii be repeated	Too cycles.
	POSDO	Conditions of steps for	or 1 cycle	
	CRITY	Step Tempera		e(min)
	10	1 1 3 -40±3 de	-)±3
		2 Room Te	•	ximum
		3 85±2 de)±3
		4 Room Te	emp 3 max	ximum
Low	△L/L:within±10%	The test samples sha		
Temperature life Test	No abnormality observed	The reflow soldering After that, the test sa		
1631	In appearance	Conditions as shown		שני מני וכטנ
		Temperature	-40±2 deg C	
		Time	500 +24/-0 h	

	Test Item	Standard	Te	est method
	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	soldering conditions shown i The test samples shall be pla	oldered to the test board by the reflo n Table 1. aced in thermostatic oven set at pplied the rated current continuous
			Temperature	85±2 deg C Rated current
			Applied current	(Refer to Page 2) 500+24/-0 h
ENVIRONMENT TESTS	test	No abnormality observed in appearance.		aced in thermostatic oven set at umidity as shown in below table. 60±2 deg C
쁘		птарреатапсе.	<u> </u>	<u>, </u>
ME			日 Humidity	90~95%RH
ENVIRON		数次	国	500+24/-0 h
	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	soldering conditions shown in the test samples shall be placed.	aced in thermostatic oven set at umidity and applied the rated curre
		Og Son	Temperature	60±2 deg C
		Opported to the second	V Diele Humidity	90~95%RH
		Opportion of the second		90~95%RH Rated current





12-5 Dimensions of packing box (for Tape & Reel package)



CONSTURCTION:

THE CASE CONTAINS 2-12mm WIDE CARRIER TAPES.

Q'TY: 2,500/ REEL



TOTAL Q'TY: 15,000 PCS