

CUSTOMER	
CUST. PART NO.	
CUST. DOC. REV.	
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
SAMPLE LOT NO.	
PART NO.	CSMV2012D-XXXX-LRH
DOC. REV.	
DATE	

Once you approve this part, please sign and return this page to the following marked location.

Customer Signature:	Date:
☐This part currently development section.	☐Production line can produce this series of products.
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TESTED BY	CHECKED BY	APPROVED BY

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	SPECIFICATION	<b>FUK</b>	APPKUVAI	1	
CUSTOMER	CUSTOMER P/N	REV.	SPL. LOT NO.		
		_			
PART NAME	PART NO.	REV.	DATE OF ISSUE	Q'TY	
POWER CHOKE			DATE OF ISSUE		PCS
(ROHS+H.F.)	CSWI V 2012D-AAAA-LKII			•	res
EN	GINEERING CHANG	GE NO	TICE - REC	ORD	
REVISION NO.	REVISION DESCRIPTIO	N	AUTHOR	DATE	REMARK
	DELECTION DIES	を TEM ALLIANCE  IECTICS  ICS (O., LTD.)	MONTS RESERVED.		

P2 Rev.A

※This is a RoHS and REACH compliant product whose related documents are available on request.

**※Graphic is only for dimensionally application.** 

### 1. Range of application:

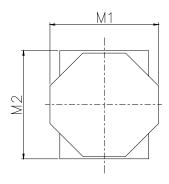
This specifications are applied to SMD Power Inductor, CSMV2012D.

#### 2. Ordering code:

Example:  $\frac{CSMV}{(1)}$   $\frac{2012}{(2)}$   $\frac{D}{(3)}$   $\frac{X}{(4)}$   $\frac{X}{(5)}$   $\frac{\Box}{(6)}$   $\frac{\Box}{(7)}$ 

- (1) Product Type
- (2) External dimensions
- (3) Solder Type
- (4) Inductance
- (5) Inductance tolerance
- (6) Green product code
- (7) Internal Code

#### 3. Mechanical Dimension:

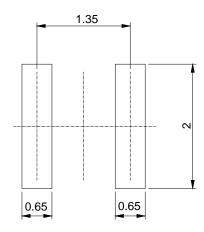




### UNIT: mm

	DIM.	TOL.
M1	2.0	±0.1
M2	2.0	±0.1
М3	1.2	MAX.
M4	0.5	±0.2
M5	1.25	±0.2

#### 4. Recommended Land-Pattern:



(Unit: mm)

#### 5. Electrical Characteristics:

	Nominal Inductance	Inductance	DC Resistance	Rated Current (mA)			
Part number	(uH) @100KHz	Tolerance	(Ω) ±20%	Saturation Current Idc1 (Typ.)	Temperature Rise Current Idc2 (Typ.)	Saturation Current Idc1 (Max.)	Temperature Rise Current Idc2 (Max.)
CSMV2012D-1R0N-LRH	1.0	±30%	0.073	2350	1830	2200	1650
CSMV2012D-1R5N-LRH	1.5	±30%	0.100	1950	1550	1800	1400
CSMV2012D-2R2M-LRH	2.2	±20%	0.129	1700	1350	1600	1200
CSMV2012D-3R3M-LRH	3.3	±20%	0.227	1350	1040	1250	900
CSMV2012D-4R7M-LRH	4.7	±20%	0.325	1150	850	1100	750

- 1. Test Frequency: 100KHz
- 2. Test Equipment:

Inductance: Chroma3302+1320+16502. or equivalent.

DCR: Chroma16502 or equivalent.

- 3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.
- 5. Rated Current: Either Idc1 or Idc2 whichever is smaller.
- 6. Operating Temperature Range:-25°C to +120°C (Including self-temperature rise)
- 7. Storage Temp. Range :  $-40^{\circ}$ C to  $+85^{\circ}$ C.

8. MSL: Level 1

#### 6. Structural Drawing:



(Magnetic Shielded Type)

① Ferrite core

Ni-Zn ferrite

② Winding wire

Polyurethane-copper wire

3 Over-coating resin

Epoxy resin, containing ferrite powder

④ Electrode

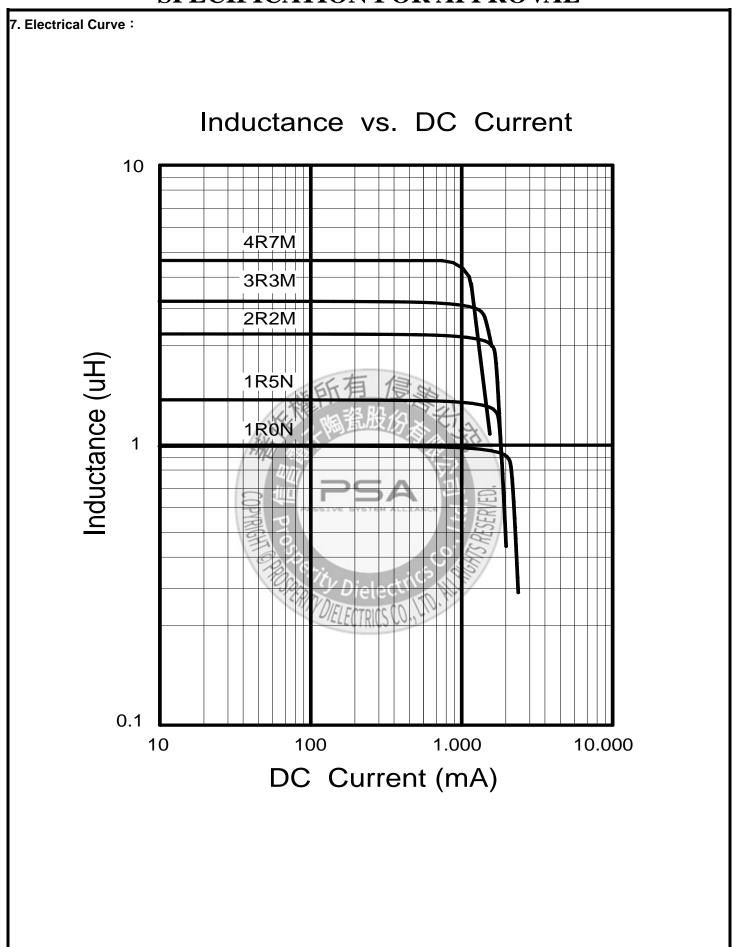
External electrode (substrate) External electrode (base plating)

External electrode (top surface solder coating)

Ag Ni-Sn

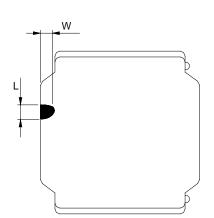
Sn-Ag-Cu

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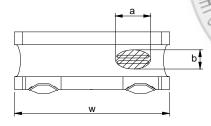
### 8. Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension

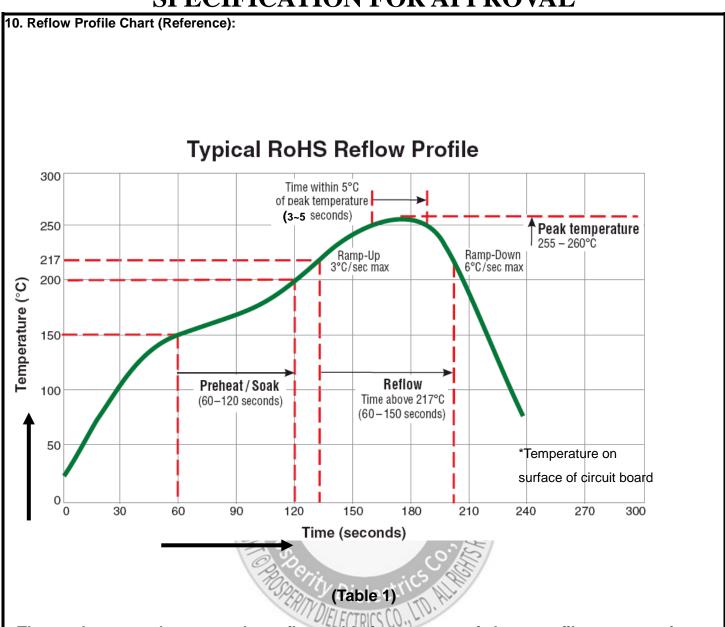


L	W	
0.4mmMax.	0.4mmMax.	

Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2
  Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
- When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

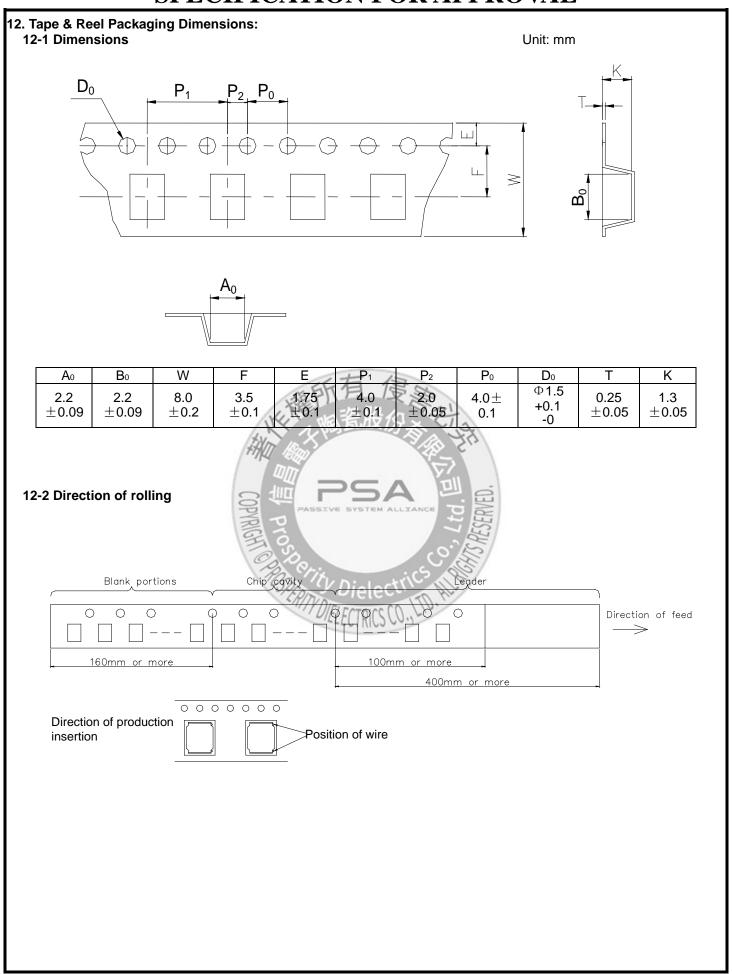


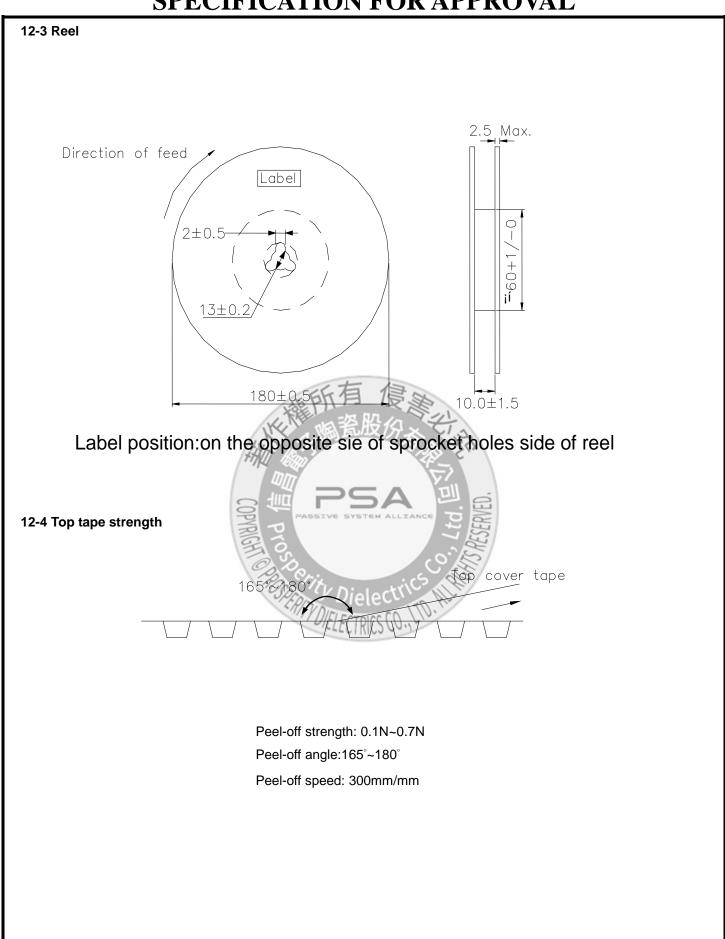
The products may be exposed to reflow soldering process of above profile up to two times.

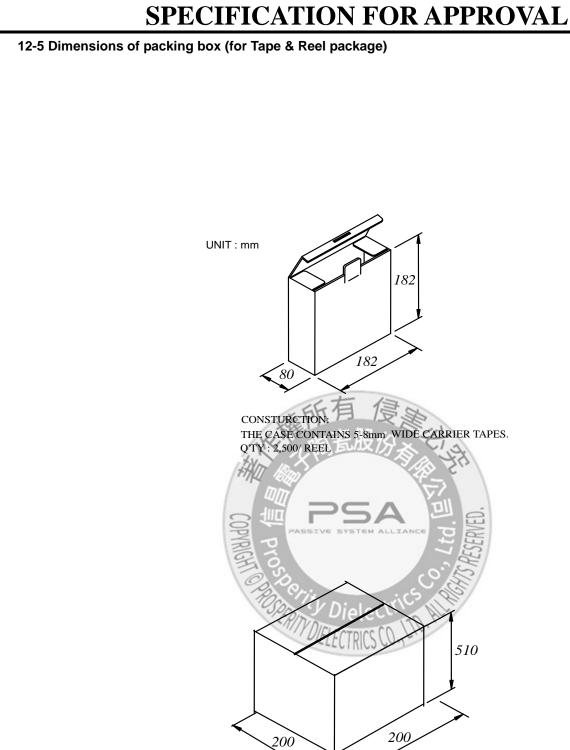
	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			Force R230 5.1
TICS			R5 — Board C C C C C C C C C C C C C C C C C C C
ERIS			Land dimensions
CTE			Test board size:100×40×10
ίRΑ			Test board material I: glass epoxy-resin Solder cream thickness:0.1 Unit: mn
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.
MEC		COPYRIGHT PYO	Applied force:10 N to X and Y directions Duration:5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s  R0.5mm Sample
			0.6W

Test Item	Standard		Test method	
Resistance to	△L/L:within±10%	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then		able 1.Then
Vibration	No abnormality	It shall be submitted t		ns
	observed	Frequency range 10		d a a a la vation
	In appearance	10tal Amplitude 19	5mm(May not exceed 96 m/S <sup>2</sup> )	
			OHz to 55Hz to 10 Hz	
		Time Fo	or 2 hours on each X,	Y, and Z axis.
Resistance to	△L/L:within±10%	The test sample shall 230±5 deg C for 40 se		
Soldering heat	No abnormality	260±5 deg C for 5 se		•
(Reflow)	observed	Test board thickness:		
	In appearance	Test board material :g	• •	
Solder ability	At least 90% of surface	The test samples sha Immersed in molten s	II be dipped in flux, ar colder as shown in bel	nd then low table.
	of terminal electrode is	Flux: Methanol solution	on containing rosin 25	
	covered by new solder.	Solder Temperature		
	VE	有 Time	5±1.0 S.	
_	138	Immersing Speed	25 mm/s	
Temperature Characteristics	△L/L:within±20%  No abnormality observed In appearance	Measurement of indu- Range within -25 deg With reference to indu- Rate shall be calculated	C to +85 deg C. uctance value at +20 ded.	deg C, change
Thermal shock	△L/L:within±10%  No abnormality observed In appearance	The test samples sha By the reflow solderin The test samples sha Shown in below table The temperature cycle Conditions of steps for	g conditions shown in Il be placed at specifi in sequence. e shall be repeated 10	n Table 1. ed
	SPERIT	Step Tempera	4/	nin)
		17 (S) -40±3 de		
		2 Room Te	emp 3 maxir	num
		3 85±2 deg	g C 30±3	3
		4 Room Te	emp 3 maxir	num
Low Temperature life Test	△L/L:within±10%  No abnormality  observed  In appearance	The test samples sha The reflow soldering of After that, the test sar Conditions as shown	conditions shown in Tamples shall be placed	able 1.
		Temperature	-40±2 deg C	
		Time	500 +24/-0 h	

	Test Item	Standard	Test method
	Loading at high temperature life test	△L/L:within±10%  No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.
			Temperature 85±2 deg C
			Applied current (Refer to Page 2)
			Time 500+24/-0 h
ENVIRONMENT TESTS	Damp heat life test	△L/L:within±10%  No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table.  Temperature 60±2 deg C
IME		V.E	日 Humidity 90~95%RH
ENVIRON		科力	Time 500+24/-0 h
	Loading under Damp heat life test	△L/L:within±10%  No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.  The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table.  Temperature 60±2 deg C Humidity 90~95%RH  Applied current (Refer to Page 2)) Time 500+24/-0 h







TOTAL Q'TY: 75,000 PCS

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